



Product Change Notification - SYST-27JLFH224

Date:

30 Mar 2020

Product Category:

16-Bit - Microcontrollers and Digital Signal Controllers

Affected CPNs:**Notification subject:**

Data Sheet - PIC24FV16KM204 Family Data Sheet

Notification text:

SYST-27JLFH224

Microchip has released a new Product Documents for the PIC24FV16KM204 Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC24FV16KM204 Family Data Sheet](#).

Notification Status: Final**Description of Change:**

- 1) Updates the device name in the 20-Pin PDIP/SSOP/SOIC pin diagram to PIC24FV08KM101.
- 2) Removes all 20-Pin QFN information from the data sheet.
- 3) Adds AVDD and AVSS pin information to the 28-pin QFN pin diagram.
- 4) Updates the note references in Table 4-25.
- 5) Updates Table 8-2 with all new content.
- 6) Updates Example 6-2.
- 7) Changes all instances of POSCMD to POSCMOD.
- 8) Adds a third note to Section 9.4.2 “Oscillator Switching Sequence”
- 9) Removes RTC content from the first paragraph in Section 12.0 “Timer1”.
- 10) Updates Section 13.0 “Capture/Compare/PWM/ Timer Modules (MCCP and SCCP)”:
 - a) Adds additional MCCP/SCCP FRM information to the first note.
 - b) Adds a note to Table 13-3.
 - c) Updates the CLKSEL bit in Register 13-1.
 - d) Updates multiple rows in Table 13-6.
 - e) Changes the OCAEN bit in Register 13-4 from R/W-0 to R/W-1
- 11) Adds additional MSSP FRM information to the first note in Section 14.0 “Master Synchronous Serial Port (MSSP)”.
- 12) Adds CLC FRM information to the beginning of Section 17.0 “Configurable Logic Cell (CLC)”.
- 13) Updates Register 17-4 with corrected inverted signal information.
- 14) Updates Capture/Compare Event or Timer information for SSRC bit in Register 19-1.
- 15) Corrects CHH, CSS and CTMEN bit locations in Register 19-6, Register 19-8 and Register 19-10.
- 16) Updates Section 20.0 “8-Bit Digital-to-Analog Converter (DAC)”:
 - a) Removes the first note and removes the buffered output voltage bullet.
- 17) Changes the CREF bit range from CREF[4-3] to CREF[5-4] and increases the unimplemented bit range to 3-2 in Register 22-1.
- 18) Adds a note to Section Register 25-4: “FOSC: Oscillator Configuration Register”.
- 19) Updates the following tables in Section 27.0 “Electrical Characteristics”:
 - a) Table 27-2, Table 27-4, Table 27-15, Table 27-17, Table 27-22 and Table 27-39.
- 20) Other minor typographical corrections throughout the document.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete



Date Document Changes Effective: 30 Mar 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

[PIC24FV16KM204 Family Data Sheet](#)

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