



Product Change Notification - SYST-14SSVQ730

Date:

17 Feb 2020

Product Category:

Microprocessors

Affected CPNs:**Notification subject:**

ERRATA - SAMA5D2 System in Package (SiP) Silicon Errata and Data Sheet Clarifications

Notification text:

SYST-14SSVQ730

Microchip has released a new Product Documents for the SAMA5D2 System in Package (SiP) Silicon Errata and Data Sheet Clarifications of devices. If you are using one of these devices please read the document located at [SAMA5D2 System in Package \(SiP\) Silicon Errata and Data Sheet Clarifications](#).

Notification Status: Final**Description of Change:**

- 1 Added in 10. Real-Time Clock (RTC):
 - 10.1 RTC_SR.TDERR flag is stuck at 0
 - 10.2 Read access truncated to the first 24 bits for register RTC_TIMALR (UTC_MODE)
- 2. Updated in 11. ROM Code:
 - 11.1 UART blocks USB connection to SAM-BA Monitor

Impacts to Data Sheet: None**Reason for Change:** To Improve Productivity**Change Implementation Status:** Complete**Date Document Changes Effective:** 17 February 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A**Attachment(s):**

[SAMA5D2 System in Package \(SiP\) Silicon Errata and Data Sheet Clarifications](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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