



Product Change Notification - SYST-08WVEY132

Date:

09 Jan 2020

Product Category:

32-bit Microcontrollers

Affected CPNs:**Notification subject:**

ERRATA - SAM G55 Series Family Silicon Errata and Data Sheet Clarifications

Notification text:

SYST-08WVEY132

Microchip has released a new Product Documents for the SAM G55 Series Family Silicon Errata and Data Sheet Clarifications of devices. If you are using one of these devices please read the document located at [SAM G55 Series Family Silicon Errata and Data Sheet Clarifications](#).

Notification Status: Final**Description of Change:**

This revision includes the following updates to Data Sheet Clarifications:

- 1) Module: Electrical Characteristics - Recommended Operating Conditions
- 2) Module: USB Host Port (UHP): User Interface
- 3) Module: System I/O Lines -Table 9-6 “System I/O Configuration Pin List”
- 4) Module: Reset Controller (RSTC)
- 5) Module: Product Mapping
- 6) Module: Supply Controller (SUPC)
- 7) Module: General Purpose Backup Registers (GPBR)
- 8) The offset listed for SYS_GPBR7 in Table 28-1. Register Mapping must be 0x1C.

Impacts to Data Sheet: None**Reason for Change:** To Improve Productivity**Change Implementation Status:** Complete**Date Document Changes Effective:** 09 Jan 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A**Attachment(s):**

[SAM G55 Series Family Silicon Errata and Data Sheet Clarifications](#)

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