MICROCHIP

Product Change Notification - RMES-16ZASY060

Date:

25 Jun 2019

Product Category:

Ethernet PHYs

Affected CPNs:

Notification subject:

Data Sheet - VSC8572XKS-02/05 and VSC8574XKS-02/05

Notification text:

Microchip has released a new DeviceDoc for the Dual-Port and Quad-Port 10/100/1000BASE-T PHY with Synchronous Datasheet of devices.

If you are using one of these devices please read the document located at VSC8572-02 Datasheet and VSC8574-02 Datasheet.

Notification Status:

Final

Description of Change:

PPS functionality was removed from the following sections and figures:

1. Block Diagram
2. IEEE 1588 Architecture figure
3. IEEE 1588 Device Synchronization section
4. Timestamp Update section
5. Local Time Counter section
6. Register Bits for GPIO Control and Status table
7. GPIO Control 2, Address 14G (0x0E) table
8. GPIO Input, Address 15G (0x0F) table
9. GPIO Output, Address 16G (0x10) table
10. GPIO Input/Output Configuration, Address 17G (0x11) table
11. Register Groups in IP_1588 table
12. Registers in IP_1588_LTC table
13. Fields in LTC_CTRL table
14. 1588 Support Pins table
15. GPIO and 1588 Support Pins table
16. GPIO and SIGDET Pins table

The following sections were deleted.

1. 1588_PPS_0/1 Mux Control section.
2. IP_1588:IP_1588_LTC:LTC_1PPS_WIDTH_ADJ section
3. IP_1588:MISC_CFG section

The 10BASE-T link recovery failures Design Consideration was corrected.

VSC8572-02

VSC8574-02

Impacts to Data Sheet:

None

Reason for Change:

To Improve Manufacturability

Change Implementation Status:

Complete

Date Document Changes Effective:
25 Jun 2019

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices:
N/A
Attachment(s):

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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