Product Change Notification: SYST-30YPCU482

Date:

01-Oct-2026

Product Category:

Clock And Timing - Clock And Data Distribution

Notification Subject:

Data Sheet - ZL40294B - 20-Output DB2000QL PCIe Buffer with Ultra-Low Additive Jitter

Affected CPNs:

SYST-30YPCU482_Affected_CPN_10012026.pdf
SYST-30YPCU482_Affected_CPN_10012026.csv

Notification Text:

SYST-30YPCU482

Microchip has released a new Datasheet for the ZL40294B - 20-Output DB2000QL PCIe Buffer with Ultra-Low Additive Jitter of devices. If you are using one of these devices please read the document located at **ZL40294B - 20-Output DB2000QL PCIe Buffer with Ultra-Low Additive Jitter**.

Notification Status: Final

Description of Change:

In Table 5-12 and Table 5-13, updated thermal values in both tables. In Table 2-1, updated descriptions for input references G1 and H1 as well as for output clocks J1-F1. In Section 3.1, added new paragraph near top of this section. In Table 4-11, added new register table. In Table 5-5, corrected the 156.25 MHz clock typical values for Additive RMS Jitter in 1 MHz to 20 MHz band and for Additive RMS Jitter in 12 kHz to 20 MHz band. In Section 6.1, updated Package Marking Drawing and legend with e4 instead of e3 for the JEDEC designator.

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 01 Oct 2026

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL40294B - 20-Output DB2000QL PCIe Buffer with Ultra-Low Additive Jitter

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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