



Product Change Notification / SYST-29JGTL971

Date:

30-Oct-2020

Product Category:

Radiation Tolerance

PCN Type:

Document Change

Notification Subject:

Data Sheet - VSC8541RT Single Port Gigabit Ethernet Copper PHY with GMII/RGMII/MII/RMII Interfaces
Document Revision

Affected CPNs:

[SYST-29JGTL971_Affected_CPN_10302020.pdf](#)
[SYST-29JGTL971_Affected_CPN_10302020.csv](#)

Notification Text:

SYST-29JGTL971

Microchip has released a new Product Documents for the VSC8541RT Single Port Gigabit Ethernet Copper PHY with GMII/RGMII/MII/RMII Interfaces of devices. If you are using one of these devices please read the document located at [VSC8541RT Single Port Gigabit Ethernet Copper PHY with GMII/RGMII/MII/RMII Interfaces](#).

Notification Status: Final

Description of Change:

The following is a summary of changes in revision C of this document.

- 1) Updated the Mode 2 section with external clock frequency values. For more information, see Section 5.2.3.2.2, Mode 2.
- 2) Updated the note under the ABSOLUTE MAXIMUM RATINGS table. For more information, see Table 8-1.
- 3) Updated the VOLTAGE MODE LINE DRIVER section with updated figures. For more information, see Section 5.4.1, Voltage Mode Line Driver.
- 4) Updated the THERMAL SPECIFICATIONS section. For more information, see Section 9.1.2, Thermal Specifications.
- 5) Updated the THERMAL SPECIFICATIONS section. For more information, see Section 9.2.2, Thermal Specifications.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 30 Oct 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

VSC8541RT Single Port Gigabit Ethernet Copper PHY with GMII/RGMII/MII/RMII Interfaces

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