



## Product Change Notification: SYST-28GHEF359

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**Date:**

03-Jul-2025

**Product Category:**

32-Bit Microprocessors

**Notification Subject:**

Data Sheet - SAMA7D6 Series Data Sheet

**Affected CPNs:**

[SYST-28GHEF359\\_Affected\\_CPN\\_07032025.pdf](#)

[SYST-28GHEF359\\_Affected\\_CPN\\_07032025.csv](#)

**Notification Text:**

SYST-28GHEF359
<p>Microchip has released a new Datasheet for the SAMA7D6 Series Data Sheet of devices. If you are using one of these devices please read the document located at <a href="#">SAMA7D6 Series Data Sheet</a>.</p> <p><b>Notification Status:</b> Final</p> <p><b>Description of Change:</b></p> <p>General -Added content related to Extended Industrial and Automotive devices SAMA7D65(T)-E/4HB(-SLx)(VAO)</p> <p>Cortex-A7 Processor (Arm) - Updated Reference Documents, Renamed and reworked section System Counter and Generic Timer</p> <p>System Interconnect and Security (SIS) - Updated AHB, Table 14.1, Table 14.10</p> <p>Debug and Test - Updated Figure 17.1</p> <p>External Interrupt Controller (EIC) EIC_SCFGxR: updated reset value</p>

Boot Strategies - Reworked NVM Boot Sequence, Booting with OTP Emulation Mode, SAM-BA Monitor Serial Connection, Updated Using a Boot Configuration Packet, Boot Configuration Packet, Software Considerations, Device Specific Data for User Application

Analog-to-Digital Converter (ADC) Controller - Added ADC Input Sampling Capacitor Charging Time (Tracking Time), Maximum Sampling Frequency, Analog-to-Digital Conversion: updated tracking time information, ADC\_MR: updated TRACKTIM description

LCD Controller (LDC) - Embedded Characteristics: updated Resolution characteristic

Display Serial Interface (DSI) - High-Speed Frequency Range Control Operation: updated Table 50.3, D-PHY PLL Control Operation: updated Table 50.4

Triple Data Encryption Standard (TDES) - Updated Security and Safety Analysis and Reports, TDES\_MR: modified PKRS description

Physical Unclonable Functions (PUF) - PUF\_ASIER, PUF\_ASISR: added bit KELEE

Quad Serial Peripheral Interface (QSPI) - Added End of Frame Procedure

Secure Digital MultiMedia Card Controller (SDMMC) - SDMMC\_MC3R: corrected DQSUPVAL position

USB Device High Speed Port (UDPHS) - Updated Power Management, Updated Important note in Transfer Without DMA

Electrical Characteristics - Updated ESD Ratings

**Impacts to Data Sheet:** None

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 03 July 2025

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

[SAMA7D6 Series Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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