



Product Change Notification: SYST-28CVHR807

Date:

10-Feb-2025

Product Category:

32-Bit Microprocessors

Notification Subject:

SAM9X60 Device Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-28CVHR807_Affected_CPN_02102025.pdf](#)

[SYST-28CVHR807_Affected_CPN_02102025.csv](#)

Notification Text:

SYST-28CVHR807

Microchip has released a new Document for the SAM9X60 Device Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at **[SAM9X60 Device Silicon Errata and Data Sheet Clarification](#)**.

Notification Status: Final

Description of Change:

Throughout:

- “Silicon revision A0” now referred to as “Device revision A2”
- “Silicon revision A1” now referred to as “Device revision A1”

Added:

- Card Detect on SDMMC not functional
- RSTC_SR.RSTTYP not showing GENERAL_RST
- SPLIP mode does not work with some header sizes

Data Sheet Clarifications: removed “OTPC Dependency to Main RC Oscillator” issue

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 10 Feb 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

SAM9X60 Device Silicon Errata and Data Sheet Clarification

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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