



Product Change Notification: SYST-28AYXH645

Date:

17-Aug-2026

Product Category:

Clock And Timing - Clock Generation

Notification Subject:

Data Sheet - ZL30270/1 - 6- or 10-Output, 5-Synthesizer Clock Generators

Affected CPNs:

[SYST-28AYXH645_Affected_CPN_08172026.pdf](#)

[SYST-28AYXH645_Affected_CPN_08172026.csv](#)

Notification Text:

SYST-28AYXH645
Microchip has released a new Datasheet for the ZL30270/1 - 6- or 10-Output, 5-Synthesizer Clock Generators of devices. If you are using one of these devices please read the document located at ZL30270/1 - 6- or 10-Output, 5-Synthesizer Clock Generators. Notification Status: Final Description of Change: Added PCIe 6 and 7 to the PCIe bullet in Section 2.3. Added PCIe jitter specs to Table 10-1. Added "or VSS if unused" to all VDDO descriptions in Table 4-1. Impacts to Data Sheet: See above details. Reason for Change: To Improve Productivity Change Implementation Status: Complete Date Document Changes Effective: 17 Aug 2026 NOTE: Please be advised that this is a change to the document only the product has not been

Description of Change:

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 17 Aug 2026

NOTE: Please be advised that this is a change to the document only the product has not been

changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL30270/1 - 6- or 10-Output, 5-Synthesizer Clock Generators

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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