



Product Change Notification: SYST-26YJNJ889

Date:

27-Feb-2025

Product Category:

Clock and Timing - Clock and Data Distribution

Notification Subject:

Data Sheet - ZL40241 - Ten LVCMOS Output Low Additive Jitter Fanout Buffer

Affected CPNs:

[SYST-26YJNJ889_Affected_CPN_02272025.pdf](#)

[SYST-26YJNJ889_Affected_CPN_02272025.csv](#)

Notification Text:

SYST-26YJNJ889

Microchip has released a new Datasheet for the ZL40241 - Ten LVCMOS Output Low Additive Jitter Fanout Buffer of devices. If you are using one of these devices please read the document located at **[ZL40241 - Ten LVCMOS Output Low Additive Jitter Fanout Buffer](#)**

Notification Status: Final

Description of Change: Updated input duty cycle row and added note 5 to Table 3-4.

Impacts to Data Sheet: See above details.

Reason for Change: To improve productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 27 Feb 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL40241 - Ten LVCMOS Output Low Additive Jitter Fanout Buffer

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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