



Product Change Notification: SYST-26UDKR428

Date:

28-Aug-2026

Product Category:

Clock Buffers

Notification Subject:

Data Sheet - ZL40292 - 20-Output DB2000Q Buffer with Ultra-Low Additive Jitter

Affected CPNs:

[SYST-26UDKR428_Affected_CPN_08282026.pdf](#)

[SYST-26UDKR428_Affected_CPN_08282026.csv](#)

Notification Text:

SYST-26UDKR428

Microchip has released a new Datasheet for the ZL40292 - 20-Output DB2000Q Buffer with Ultra-Low Additive Jitter of devices. If you are using one of these devices please read the document located at [ZL40292 - 20-Output DB2000Q Buffer with Ultra-Low Additive Jitter](#).

Notification Status: Final

Description of Change:

Features: Updated Jitter values in the third bullet.

Applications: Added PCIe Gen 7 to the first bullet.

Table 3-6: Updated defaults for Bits 5 and 6.

Table 4-5: Updated title and multiple values.

Table 4-6: Added this table.

Impacts to Data Sheet: See above details

Reason for change: To improve productivity

Change Implementation Status: Complete

Date Document Changes Effective: 28 Aug 2026

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL40292 - 20-Output DB2000Q Buffer with Ultra-Low Additive Jitter

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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