



Product Change Notification: SYST-26QAVH131

Date:

22-Sep-2026

Product Category:

Clock Buffers

Notification Subject:

Data Sheet - ZL40293 - 20-Output PCIe Gen 1/2/3/4/5/6 Buffer with Ultra-Low Additive Jitter

Affected CPNs:

[SYST-26QAVH131_Affected_CPN_09222026.pdf](#)

[SYST-26QAVH131_Affected_CPN_09222026.csv](#)

Notification Text:

SYST-26QAVH131

Microchip has released a new Datasheet for the ZL40293 - 20-Output PCIe Gen 1/2/3/4/5/6 Buffer with Ultra-Low Additive Jitter of devices. If you are using one of these devices please read the document located at [ZL40293 - 20-Output PCIe Gen 1/2/3/4/5/6 Buffer with Ultra-Low Additive Jitter](#).

Notification Status: Final

Description of Change:

Features: Updated jitter values in the third bullet. Updated supported PCIe generation in first bullet.

Applications: Added PCIe Gen 7 to the first bullet.

Table 3-6, Table 3-7: Updated some default values.

Table 4-5: Updated peak-to-peak additive jitter maximum value.

Table 4-6: Updated title and some values.

Table 4-7: Added this table.

Impacts to Data Sheet: See above details

Reason for change: To improve productivity

Change Implementation Status: Complete

Date Document Changes Effective: 28 Aug 2026

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Revision History:

August 28, 2026: Issued document PCN.

September 22, 2026: re-issued document PCN to update affected parts.

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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