



Product Change Notification: SYST-26JBPZ425

Date:

02-Oct-2025

Product Category:

Clock And Timing - Clock And Data Distribution

Notification Subject:

Data Sheet - ZL40294B - 20-Output DB2000QL Buffer with Ultra-Low Additive Jitter

Affected CPNs:

[SYST-26JBPZ425_Affected_CPN_10022025.pdf](#)

[SYST-26JBPZ425_Affected_CPN_10022025.csv](#)

Notification Text:

SYST-26JBPZ425

Microchip has released a new Datasheet for the ZL40294B - 20-Output DB2000QL Buffer with Ultra-Low Additive Jitter of devices. If you are using one of these devices please read the document located at [ZL40294B - 20-Output DB2000QL Buffer with Ultra-Low Additive Jitter](#).

Notification Status: Final

Description of Change:

Features: Added PCIe Gen 7.0 information.

Applications: Added PCIe Gen 7.0 information.

Table 5-5: Added values for PCIe Gen 7.0 performance.

Table 5-6: Added values for PCIe Gen 7.0 performance.

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 02 Oct 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL40294B - 20-Output DB2000QL Buffer with Ultra-Low Additive Jitter

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to receive Microchip PCNs via email please register for our PCN email service at our [PCN home page](#) select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the [PCN FAQ](#) section.

If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from