



Product Change Notification / SYST-26CDBH499

Date:

27-Aug-2020

Product Category:

8-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

Data Sheet - AVR128DA28/32/48/64 Preliminary Data Sheet Document Revision

Affected CPNs:

[SYST-26CDBH499_Affected_CPN_08272020.pdf](#)

[SYST-26CDBH499_Affected_CPN_08272020.csv](#)

Notification Text:

SYST-26CDBH499

Microchip has released a new Product Documents for the AVR128DA28/32/48/64 Preliminary Data Sheet of devices. If you are using one of these devices please read the document located at [AVR128DA28/32/48/64 Preliminary Data Sheet](#).

Notification Status: Final

Description of Change:

- 1) Entire Document: a) Updated AVR® MCU DA (AVR-DA) to AVR® DA MCU, and AVR-DA to AVR DA, per latest trademarking b) Editorial updates
- 2) Hardware Guidelines: Changed the value of the primary decoupling capacitor from 100 nF to 1 µF
- 3) Sleep Controller: Added the High-Temperature Low Leakage Enable (HTLLEN) bit in the VREGCTRL register
- 4) Electrical characteristics: a) Added Supply Voltage Slew Rate b) Updated I/O Pin characteristics (Input Leakage current)

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 27 Aug 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[AVR128DA28/32/48/64 Preliminary Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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