



Product Change Notification: SYST-25XYWT913

Date:

20-Oct-2025

Product Category:

Clock Buffers

Notification Subject:

Data Sheet - SY75602A/02B/603A/03B/604A/04B - 2/4 Outputs Ultra-Low Additive Jitter PCI Express Clock Buffers

Affected CPNs:

[SYST-25XYWT913_Affected_CPN_10202025.pdf](#)
[SYST-25XYWT913_Affected_CPN_10202025.csv](#)

Notification Text:

SYST-25XYWT913

Microchip has released a new Datasheet for the SY75602A/02B/603A/03B/604A/04B - 2/4 Outputs Ultra-Low Additive Jitter PCI Express Clock Buffers of devices. If you are using one of these devices please read the document located at [SY75602A/02B/603A/03B/604A/04B - 2/4 Outputs Ultra-Low Additive Jitter PCI Express Clock Buffers](#).

Notification Status: Final

Description of Change:

- Updated Features and General Description with PCIe Gen 7 support information.
- Added PCIe Gen 7 performance information and values to the Jitter and Phase Noise table.
- Updated Functional Description with PCIe Gen 7 support information.
- Corrected a typo in Figure 4-3.

Impacts to Data Sheet: See above details

Change Implementation Status: Complete

Date Document Changes Effective: 20 Oct 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

SY75602A/02B/603A/03B/604A/04B - 2/4 Outputs Ultra-Low Additive Jitter PCI Express Clock Buffers

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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