



Product Change Notification: SYST-25UOZN260

Date:

26-Jun-2025

Product Category:

32-Bit Microcontrollers

Notification Subject:

Data Sheet - PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet

Affected CPNs:

[SYST-25UOZN260_Affected_CPN_06262025.pdf](#)

[SYST-25UOZN260_Affected_CPN_06262025.csv](#)

Notification Text:

SYST-25UOZN260

Microchip has released a new Datasheet for the PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet](#).

Notification Status: Final

Description of Change:

Revision G - June 2025

Section Description

Up to 2 MB Live-Update Flash and 512 KB SRAM with Hardware Security Module (HSM) for Secure Connectivity Applications

- Added in new temperature range for Operating Conditions

Package and Pinout

- Updates to Signal Names for the EIC, PTC, and CAN were implemented for the following tables:
 - 48-Pin VQFN Packages
 - 64-Pin TQFP/VQFN Packages
 - 100-Pin TQFP Package
 - 144-Pin TQFP Package
 - 144-Pin TFBGA Package

Signal Description

- Replaced the Serial Communication Interface (SERCOM) Signals table with a new table

Multi-Channel RAM Controller (MCRAMC)

- All new updated chapter

Clock Distribution System

- Added a new note to Overview for asynchronicity

PORT

- Added the following Registers:

- INTENCLR
- INTENSET
- INTFLAG
- NONSEC
- NSCHK

WDT

- Updated the EWOFFSET bitfield of the EWCTRL Register

with a new signal name, replacing GCLK_WDT with CLK_WDT_OSC

DMAC

- Updated the Register output for all Channel k Registers to properly display in the Register Summary

ETH

- Updated the table in Signal Interface, replacing ETH_TXCK1 with ETH_TX_CLK

SERCOM USART

- Updated the bit value from 8 to 16 in the Clock Generation Diagram in Clock Generation and Selection
- Updated Parity to include 0x5 for error checking
- Updated the PMODE bit in the CTRLB Register

SERCOM I2C

- Added a Table to the Block Diagram for I2C Pad Descriptions.

TCC

- Updated the Diagram for the Circular Buffer

Electrical Characteristics 85°C

- Added note 4 to the following sections:
 - Non-Volatile Memory Controller (NVM) Electrical Specifications
 - Universal Serial Bus (USB) Electrical Specifications
- Added in new diagrams to the following sections:
 - MCU Active Power
 - MCU Idle Power
 - MCU Standby Power

Electrical Characteristics 125°C

- All new Electrical Specifications chapter

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 26 Jun 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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