



Product Change Notification: SYST-25PUOR100

Date:

20-Oct-2025

Product Category:

Clock And Timing - Clock Generation

Notification Subject:

Data Sheet - ZL30291B - CK440Q Compliant PCI Express and Platform Timing Clock Generator

Affected CPNs:

[SYST-25PUOR100_Affected_CPN_10202025.pdf](#)

[SYST-25PUOR100_Affected_CPN_10202025.csv](#)

Notification Text:

SYST-25PUOR100
<p>Microchip has released a new Datasheet for the ZL30291B - CK440Q Compliant PCI Express and Platform Timing Clock Generator of devices. If you are using one of these devices please read the document located at ZL30291B - CK440Q Compliant PCI Express and Platform Timing Clock Generator.</p> <p>Notification Status: Final</p> <p>Description of Change:</p> <p>Features: Added PCIe Gen 7.0 information.</p> <p>Applications: Added PCIe Gen 7.0 information.</p> <p>Section 2.1: Added PCIe Gen 7.0 information.</p> <p>Table 4-7: Added values for PCIe Gen 7.0 performance.</p> <p>Impacts to Data Sheet: See above details.</p>

Description of Change:

Features: Added PCIe Gen 7.0 information.

Applications: Added PCIe Gen 7.0 information.

Section 2.1: Added PCIe Gen 7.0 information.

Table 4-7: Added values for PCIe Gen 7.0 performance.

Impacts to Data Sheet: See above details.

Change Implementation Status: Complete

Date Document Changes Effective: 20 Oct 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL30291B - CK440Q Compliant PCI Express and Platform Timing Clock Generator

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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