



Product Change Notification / SYST-25IQR096

Date:

28-Sep-2020

Product Category:

Clock and Timing - Clock and Data Distribution

PCN Type:

Document Change

Notification Subject:

Data Sheet - PL130-07 - High Speed Translator Inverting Buffer to LVCMOS

Affected CPNs:

[SYST-25IQR096_Affected_CPN_09282020.pdf](#)

[SYST-25IQR096_Affected_CPN_09282020.csv](#)

Notification Text:

SYST-25IQR096

Microchip has released a new Product Documents for the PL130-07 - High Speed Translator Inverting Buffer to LVCMOS of devices. If you are using one of these devices please read the document located at [PL130-07 - High Speed Translator Inverting Buffer to LVCMOS](#)

Notification Status: Final

Description of Change: Added the word "Inverting" to the data sheet title and updated the Block Diagram.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 28 Sep 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

PL130-07 - High Speed Translator Inverting Buffer to LVCMOS

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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