



## Product Change Notification: SYST-25CGAF651

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### Date:

28-Oct-2024

### Product Category:

32-Bit Microcontrollers

### Notification Subject:

Data Sheet - PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet

### Affected CPNs:

**SYST-25CGAF651\_Affected\_CPN\_10282024.pdf**

**SYST-25CGAF651\_Affected\_CPN\_10282024.csv**

### Notification Text:

SYST-25CGAF651

Microchip has released a new Datasheet for the PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet of devices. If you are using one of these devices please read the document located at **PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet**.

**Notification Status:** Final

#### Description of Change:

- Configuration Summary: Added new tables to reflect new package offerings
- Package and Pinout: Added a new pinout for the 144-pin TFBGA and 48-pin VQFN
- Signal Descriptions: Updated all tables to display new pin counts for the 48-pin TQFP and 144-pin TFBGA
- Power Supplies and Startup Considerations: Updated Table 7-1 with new signal names; Updated the Power Domain Block Diagram with new signal names; Updated Power Domain Constraints with new signal names; Updated Signal names for figure 7-2 in Typical Powering Schematic; Updated signal names in Supply Order
- Memories: Added a note to FUCFG3

- SUPC: Added a note to Overview; Updated the Block Diagram with new signal names
- RSTC: Updated the BORVDDA bitfield of the RCAUSE Register with a new note
- PORT: Added a new table to the PMUXm Register
- TCC: Added timers descriptions to the Overview; Added a new column for Counter Size to the Feature Summary; Updated the number of comparators in the Block Diagram; Added notes to the following Registers: PER, CCy, PERBUF, CCBUFy
- CCL: Added a note to the INSELy bitfield of the LUTCTRLn Register
- ADC: Updated the input Signal names from AIN to VINP throughout the chapter
- Electrical Characteristics: Added in the 144-pin TFBGA package for the Thermal Packaging Characteristics table; Updated signal names in Analog-to-Digital Converter (ADC) Electrical Specifications
- Packaging: Added a new 48-pin VQFN; Added a new 144-pin TFBGA Package

**Impacts to Data Sheet:** See above details.

**Reason for Change:** To improve productivity.

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 28 Oct 2024

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

### **PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet**

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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