



Product Change Notification: SYST-24JLEZ857

Date:

27-Sep-2025

Product Category:

Clock Buffers

Notification Subject:

Data Sheet - ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control

Affected CPNs:

[SYST-24JLEZ857_Affected_CPN_09272025.pdf](#)

[SYST-24JLEZ857_Affected_CPN_09272025.csv](#)

Notification Text:

SYST-24JLEZ857

Microchip has released a new Datasheet for the ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control of devices. If you are using one of these devices please read the document located at [ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control](#).

Notification Status: Final

Description of Change:

Applications: Added support information for PCIe Gen 7.

Table 4-13: Added performance values for PCIe Gen 7.

Table 4-14: Added performance values for PCIe Gen 7.

Impacts to Data Sheet: See above details.

Change Implementation Status: Complete

Date Document Changes Effective: 27 Sep 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control

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