



Product Change Notification / SYST-22TOEC713

Date:

23-Apr-2021

Product Category:

Memory

PCN Type:

Document Change

Notification Subject:

Data Sheet - AT24C512C - I2C-Compatible (Two-Wire) Serial EEPROM 512 Kbit (65,536x8) Datasheet

Affected CPNs:

[SYST-22TOEC713_Affected_CPN_04232021.pdf](#)

[SYST-22TOEC713_Affected_CPN_04232021.csv](#)

Notification Text:

SYST-22TOEC713

Microchip has released a new Product Documents for the AT24C512C - I2C-Compatible (Two-Wire) Serial EEPROM 512 Kbit (65,536x8) Datasheet of devices. If you are using one of these devices please read the document located at [AT24C512C - I2C-Compatible \(Two-Wire\) Serial EEPROM 512 Kbit \(65,536x8\) Datasheet](#).

Notification Status: Final

Description of Change:

1. Replaced terminology "Master" and "Slave" with "Host" and "Client", respectively. Added note to Table 4-6 that explains the array architecture and how endurance is specified. Updated TSSOP and UDFN package drawings.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 23 Apr 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

AT24C512C - I2C-Compatible (Two-Wire) Serial EEPROM 512 Kbit (65,536x8) Datasheet

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