



Product Change Notification / SYST-22MXTJ296

Date:

27-Oct-2020

Product Category:

USB Hubs

PCN Type:

Document Change

Notification Subject:

Data Sheet - USB7216 6-Port USB 3.2 Gen 2 Type-C Controller Hub Data Sheet Document Revision

Affected CPNs:

[SYST-22MXTJ296_Affected_CPN_10272020.pdf](#)

[SYST-22MXTJ296_Affected_CPN_10272020.csv](#)

Notification Text:

SYST-22MXTJ296

Microchip has released a new Product Documents for the USB7216 6-Port USB 3.2 Gen 2 Type-C Controller Hub of devices. If you are using one of these devices please read the document located at [USB7216 6-Port USB 3.2 Gen 2 Type-C Controller Hub](#).

Notification Status: Final

Description of Change:

- 1) Cover
 - Added bullet: "Programming of firmware image to external SPI memory device from USB host."
 - Updated "Configuration programming via OTP ROM..." to "Configuration programming via OTP Memory"
 - Updated "Enhanced OEM configuration options available through either OTP or SPI ROM" to "Enhanced OEM configuration options available through either OTP or external SPI memory"
 - Updated USB Type-CTM to a registered trademark, USB Type-C®
- 2) All. Updated USB 3.1 references to USB 3.2 per latest USB IF guidelines.
- 3) Table 1-2. Added AIO buffer type.
- 4) Section 1.3, Pin Reset States. Added new pin reset state section/table.
- 5) Figure 2-1 Modified the block diagram
- 6) Section 3.1, Pin Assignments. Added pin reset states to pin table.
- 7) Table 3-1, Pin Descriptions
 - Added "If Unused" column and information.

- Updated DP1_VBUS_MON buffer type to AIO, updated description and figure.
- Updated VBUS_MON_UP buffer type to AIO and updated description
- 8) Section 3.3.4, PF[31:3] Configuration (CFG_STRAP[2:1]). Updated PF29 assignment.
- 9) Table 3-6, Programmable Functions Descriptions. Added pin reset states to programmable function descriptions.
- 10) Section 3.0, Pin Descriptions, Figure 3-1, Table 3-1, Section 9.6.1, Power Supply and RESET_N Sequence Timing, Table 9-4. Removed VBUS_DET function (from pin 2)
- 11) Section 5.1.7, OTP Configuration Stage (CFG_OTP). Added note.
- 12) Section 7.1, SPI/SQI Master Interface. Added new second paragraph.
- 13) Section 8.2.1, Port Power Control using USB Power Switch. Added note after first paragraph.
- 14) Section 8.2.2, Port Power Control using Poly Fuse. Added note after first paragraph.
- 15) Section 8.3, CC Pin Orientation and Detection. Updated section description for clarity
- 16) Section 8.5, FlexConnect. Updated note.
- 17) **Section 8.6, Multi-Host Endpoint Reflector. Updated NCM reference to "NCM v1.0"**
- 18) Section 9.1, Absolute Maximum Ratings*. Added HBM ESD information.
- 19) Section 9.2, Operating Conditions**
 - Revised Digital Code and 3.3V supply voltage rise times.
 - Added note under Figure 9-1
- 20) Section 9.4, Power Consumption. Updated power consumption data.
- 21) Section 9.5, DC Specifications.
 - Updated I type buffer VIL and VIH values.
 - Updated ICLK buffer type VIH values.
- 22) Section 9.6.1, Power Supply and RESET_N Sequence Timing. Updated description.
- 23) Section 9.6.4, Power-On or Reset to SMBus Slave Ready Timing and Section 9.6.5, USB Attach Command to SMBus Slave Ready Timing. Added new sections.

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 27 Oct 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

USB7216 6-Port USB 3.2 Gen 2 Type-C Controller Hub

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