



Product Change Notification: SYST-19PIKS637

Date:

20-May-2025

Product Category:

Memory

Notification Subject:

Data Sheet - SST39SF010A/SST39SF020A/SST39SF040 - 1-Mbit/2-Mbit/4-Mbit (x8) Multi-Purpose Flash

Affected CPNs:

[SYST-19PIKS637_Affected_CPN_05202025.pdf](#)
[SYST-19PIKS637_Affected_CPN_05202025.csv](#)

Notification Text:

SYST-19PIKS637

Microchip has released a new Datasheet for the SST39SF010A/SST39SF020A/SST39SF040 - 1-Mbit/2-Mbit/4-Mbit (x8) Multi-Purpose Flash of devices. If you are using one of these devices please read the document located at [SST39SF010A/SST39SF020A/SST39SF040 - 1-Mbit/2-Mbit/4-Mbit \(x8\) Multi-Purpose Flash](#).

Notification Status: Final

Description of Change:

Corrected pin assignments for 32-Lead PLCC; Updated 32-Lead TSOP package marking.

Impacts to Data Sheet: See above details.

Change Implementation Status: Complete

Date Document Changes Effective: 20 May 2025

NOTE: Please be advised that this is a change to the document only the product has not been

changed.

Markings to Distinguish Revised from Unrevised Devices::N/A

Attachments:

SST39SF010A/SST39SF020A/SST39SF040 - 1-Mbit/2-Mbit/4-Mbit (x8) Multi-Purpose Flash

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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