



Product Change Notification: SYST-19LCUR075

Date:

20-May-2025

Product Category:

32-Bit Microcontrollers

Notification Subject:

Data Sheet - PIC32CZ CA80/CA9x High Security (SG) Embedded Connectivity Family Data Sheet

Affected CPNs:

[SYST-19LCUR075_Affected_CPN_05202025.pdf](#)

[SYST-19LCUR075_Affected_CPN_05202025.csv](#)

Notification Text:

SYST-19LCUR075

Microchip has released a new Datasheet for the PIC32CZ CA80/CA9x High Security (SG) Embedded Connectivity Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32CZ CA80/CA9x High Security \(SG\) Embedded Connectivity Family Data Sheet](#).

Notification Status: Final

Description of Change:

Up to 8 MB Flash, 1 MB SRAM, Hardware Security Module, Secure Boot, Floating Point Unit (FPU,) Advanced Analog, Gigabit Ethernet, HS USB, CANFD, and Peripheral Touch Controller (PTC)

- Updated Operating Conditions

Ordering Information

- Updated the diagram to include new temperature range

Hardware Security Module (HSM)

- Added a new section, Performance

Non-Volatile Memory Controller (NVMCTRL)

- Added CTRLB register

Gigabit Ethernet Media Access Controller (GMAC / ETH)

- Updated Signal Interface with all new content for Table 32-1
- Added a new section, Clocks

Electrical Characteristics

- Updated the following sections with new min, typ, or max specifications:
- Power Supply
- MCU Active Power
- MCU Idle Power
- MCU Standby Power

Impacts to Data Sheet: See above details

Change Implementation Status: Complete

Date Document Changes Effective: 20 May 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

PIC32CZ CA80/CA9x High Security (SG) Embedded Connectivity Family Data Sheet

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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