



Product Change Notification / SYST-17EYJ952

Date:

22-Jul-2024

Product Category:

32-Bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

Data Sheet - PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet

Affected CPNs:

[SYST-17EYJ952_Affected_CPN_07222024.pdf](#)
[SYST-17EYJ952_Affected_CPN_07222024.csv](#)

Notification Text:

SYST-17EYJ952

Microchip has released a new Datasheet for the PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet](#).

Notification Status: Final

Description of Change:

Revision D - July 2024

Section Description

5. Package and Pinout • Updated the following Pinouts updating VCCAPP3 to

VUSB3V3:

– 100-pin TQFP

– 144-pin TQFP

6. Signal Description • Updated Table 6-13 PORTA through PORTD Signals with a

new pin number for PB00

Clock Distribution System • Updated the Clock Distribution Block Diagram converting

MCLK to GCLK

48. TrustRAM (TRAM) • Updated the Overview with a new value for the security

RAM

• Updated the Address Count in the diagram for Data

Remanence Prevention

• Updated the RAM register with a new offset equation

51. Electrical Characteristics • Added eight new graphs for IOH, VOL, VOH, and IOL to

the I/O Pin Electrical Specifications

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 22 Jul 2024

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[PIC32CK SG01/SG00/GC01/GC00 Family Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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