



Product Change Notification: SYST-17BCZW035

Date:

20-Aug-2026

Product Category:

Clock Buffers

Notification Subject:

Data Sheet - ZL40231 - Low Skew, Low Additive Jitter, 10 Output LVPECL/LVDS/HCSL Fanout Buffer with One LVCMOS Output

Affected CPNs:

[SYST-17BCZW035_Affected_CPN_08202026.pdf](#)

[SYST-17BCZW035_Affected_CPN_08202026.csv](#)

Notification Text:

SYST-17BCZW035

Microchip has released a new Datasheet for the ZL40231 - Low Skew, Low Additive Jitter, 10 Output LVPECL/LVDS/HCSL Fanout Buffer with One LVCMOS Output of devices. If you are using one of these devices please read the document located at [ZL40231 - Low Skew, Low Additive Jitter, 10 Output LVPECL/LVDS/HCSL Fanout Buffer with One LVCMOS Output](#).

Notification Status: Final

Description of Change:

Added PCIe 7.0 info to Applications section on front page. Added 491.52 MHz HCSL Phase Noise image as Figure 3-13 and renumbered all images thereafter. Added additive jitter, additive RMS jitter, and noise phase values for PCIe 6.0 and PCI 7.0 to both Table 4-14 and Table 4-15. Updated back matter to most current version.

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 20 Aug 2026

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL40231 - Low Skew, Low Additive Jitter, 10 Output LVPECL/LVDS/HCSL Fanout Buffer with One LVCMOS Output

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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