



## Product Change Notification: SYST-14CAOG971

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**Date:**

01-Dec-2025

**Product Category:**

32-Bit Microprocessors

**Notification Subject:**

Data Sheet - SAMA5D2 System-in-Package (SiP) Data Sheet

**Affected CPNs:**

[SYST-14CAOG971\\_Affected\\_CPN\\_12012025.pdf](#)

[SYST-14CAOG971\\_Affected\\_CPN\\_12012025.csv](#)

**Notification Text:**

SYST-14CAOG971
<p>Microchip has released a new Datasheet for the SAMA5D2 System-in-Package (SiP) Data Sheet of devices. If you are using one of these devices please read the document located at <a href="#">SAMA5D2 System-in-Package (SiP) Data Sheet</a>.</p> <p><b>Notification Status:</b> Final</p> <p><b>Description of Change:</b></p> <p>Introduced Software License Level 3 (SL3) content.</p> <p>Configuration Summary: updated SAMA5D225 UART/SPI/I2C information.</p> <p><b>Impacts to Data Sheet:</b> See above details.</p> <p><b>Reason for change:</b> To improve productivity</p> <p><b>Change Implementation Status:</b> Complete</p>

**Description of Change:**

Introduced Software License Level 3 (SL3) content.

Configuration Summary: updated SAMA5D225 UART/SPI/I2C information.

**Impacts to Data Sheet:** See above details.

**Reason for change:** To improve productivity

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 01 December 2025

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## **Attachments:**

### **SAMA5D2 System-in-Package (SiP) Data Sheet**

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

## **Terms and Conditions:**

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