



Product Change Notification: SYST-11PEWD341

Date:

10-Dec-2025

Product Category:

8-Bit Microcontrollers

Notification Subject:

AVR32DA28/32/48(S) Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-11PEWD341_Affected_CPN_12102025.pdf](#)

[SYST-11PEWD341_Affected_CPN_12102025.csv](#)

Notification Text:

SYST-11PEWD341

Microchip has released a new Document for the AVR32DA28/32/48(S) Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [AVR32DA28/32/48\(S\) Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change:

- Added new device revision (A7)
- Added errata:
 - Device: Write Operation Lost if Consecutive Writes to Specific Address Spaces
 - ADC: ADC MUX Selection and Accumulation Number has Delayed Update When Initialization Delay is Used
 - NVMCTRL: NVM_EEPROM_ERASE Command does Not Respect Write Protect
 - TCB: TCB4 Waveform Output Alternative 1 Non-Functional
 - USART: Receiver Non-Functional after Detection of Inconsistent Synchronization Field
- Removed data sheet clarifications:
 - 3.1. Features

- 3.2. FUSE - Configuration and User Fuses
- 3.3. RSTCTRL - Reset Controller
- 3.4. TWI - Two-Wire Interface
- 3.5. Electrical Characteristics - Peripheral Power Consumption
- 3.6. Electrical Characteristics - Memory Programming Specifications
- 3.7. Electrical Characteristics - VREF
- 3.8. Electrical Characteristics - DAC
- 3.9. Electrical Characteristics - ADC

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Estimated First Ship Date: 20 Jan 2025

Date Document Changes Effective: 10 Feb 2025

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Markings to Distinguish Revised from Unrevised Devices: Traceability Code

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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