Product Change Notification: **SYST-11LBLQ130**

Date:

12-Feb-2025

Product Category:

32-Bit Microprocessors

Notification Subject:

Data Sheet - SAM9X60 Data Sheet

Affected CPNs:

SYST-11LBLQ130_Affected_CPN_02122025.pdf
SYST-11LBLQ130_Affected_CPN_02122025.csv

Notification Text:

SYST-11LBLQ130

Microchip has released a new Datasheet for the SAM9X60 Data Sheet of devices. If you are using one of these devices please read the document located at **SAM9X60 Data Sheet**.

Notification Status: Final

Description of Change:

Global
• Added content related to the SAM9X60 Series high density package devices (SAM9X60(T)-V/6GW)
• Added content related to Software License Level (SLx)
• Data sheet format reworked

Package and Pinout
Updated Figure 8.1, Table 8.2

Peripheral Identifiers
Updated Table 12.1

External Bus Interface (EBI)
Added Device (FPGA, Static Memory, etc.) on NCS0, DDR on NCS1 and NAND Flash on NCS2

Debug Unit (DBGU)
Chip Identifier: updated reset value information

OTP Memory Controller (OTPC)
OTPC_MR: corrected KBDST field width

DDR-SDRAM Controller (MPDDRC)
Updated Security and Safety Analysis and Reports
MPDDRC_WPSR: updated SEQE description

Shutdown Controller (SHDWC)
SHDW_MR: removed AUTOLPM

Debug Unit (DBGU)
DBGU_CIDR Simplified: updated reset value

Power Management Controller (PMC)
PMC_PLL_UPDT: updated PLL ID definition

Inter-IC Sound Multi-Channel Controller (I2SMCC)
Updated Figure 49.7
TDM Reception and Transmission Sequence: added note
I2SMCC_MRA: updated TDMFS description

Synchronous Serial Controller (SSC)
Updated Embedded Characteristics (added "Up to 16 Channels in TDM Mode")
Added Audio Sampling Rate Limitations

Flexible Serial Communication Controller (FLEXCOM)
Updated Baud Rate Generator, Baud Rate in Synchronous Mode, FIFO Overflow/Underflow Error, FIFO Overflow/Underflow Error, FIFO Overflow/Underflow Error
Updated TXFPTEF and RXFPTEF definitions in FLEX_US_FESR, FLEX_SPI_SR, FLEX_TWI_FSR
Added note in FLEX_US_FIDI (LON_MODE), FLEX_US_LONB1TX, FLEX_US_LONB1RX, FLEX_US_IDTTX, FLEX_US_IDTRX
FLEX_SPI_MR: added LSBHALF
Added detail on WPEN in FLEX_TWI_SMBTR

USB Device High Speed Port (UDPHS)
Updated Transfer Without DMA
Throughout, corrected number of DMA channels from 7 to 6

Analog-to-Digital Controller (ADC)
Updated Table 67.4
ADC_SEQR2: updated USCHx field width

Electrical Characteristics
Added Table 68.6, Table 68.7
Updated Figure 68.15

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 12 Feb 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

SAM9X60 Data Sheet

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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