



Product Change Notification / SYST-11IJLS246

Date:

14-Sep-2020

Product Category:

Microprocessors

PCN Type:

Document Change

Notification Subject:

Data Sheet - SAM9X60 Datasheet Document Revision

Affected CPNs:

[SYST-11IJLS246_Affected_CPN_09142020.pdf](#)
[SYST-11IJLS246_Affected_CPN_09142020.csv](#)

Notification Text:

SYST-11IJLS246

Microchip has released a new Product Documents for the SAM9X60 Datasheet of devices. If you are using one of these devices please read the document located at [SAM9X60 Datasheet](#).

Notification Status: Final

Description of Change:

- 1) Added automotive content in Features, Electrical Characteristics, Ordering Information and Product Identification System
- 2) Reset Controller (RSTC): Added NRST_OUT content
- 3) Debug Unit (DBGU): Updated ICE Access Prevention
- 4) Slow Clock Controller (SCKC): Updated Embedded Characteristics
- 5) Power Management Controller (PMC): a) Updated General Clock Distribution Block Diagram b) PMC_PLL_UPDT: corrected STUPTIM field size
- 6) DMA Controller (XDMAC): a) Transfer Hierarchy Diagrams: removed XDMAC Peripheral Transfer Hierarchy figure
b) Added Peripheral to Memory Transfer and Memory to Peripheral Transfer
- 7) LCD Controller (LCDC): a) Embedded Characteristics: updated Output mode content b) Updated YUV Frame Buffer Memory Mapping c) Renamed and updated Window Size table
- 8) 2D Graphics Engine (GFX2D): a) BLEND_WD0: added DREG field b) BLEND_WD5: replaced DFACT and SFACT by DCFACT and SCFACT, respectively; added DPRE, DFACT, SPRE and SAFACT fields c) GFX2D_GC: removed bits CGDISCORE, CGDISAXI, CGDISIFO
- 9) USB Host High Speed Port (UPHS): Corrected reset values for: a) UPHS_HCSPARAMS b) UPHS_HCCPARAMS c) UPHS_PORTSCx

- 10) Image Sensor Interface (ISI): a) Added write-protection content b) ISI_CFG2: updated RGB_SWAP and RGB_CFG bit descriptions c) ISI_Y2R_SET1: updated Yoff, Croff and Cboff bit descriptions d) ISI_R2Y_SET0: updated Roff bit description e) ISI_R2Y_SET1: updated Goff bit description f) ISI_R2Y_SET2: updated Boff bit description
- 11) Timer Counter (TC): a) Updated: Embedded Characteristics, Interrupt Sources b) Removed unrequired fault output content
- 12) Triple Data Encryption Standard (TDES): Updated TDES_MR
- 13) Electrical Characteristics: Updated Table 58-57 (CPU clock (HCLK) row)

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 14 Sep 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAM9X60 Datasheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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