



Product Change Notification / SYST-11FSNV991

Date:

13-Nov-2020

Product Category:

Interface- LIN Transceiver

PCN Type:

Document Change

Notification Subject:

Data Sheet - ATSAMHAXGXXA Data Sheet

Affected CPNs:

[SYST-11FSNV991_Affected_CPN_11132020.pdf](#)
[SYST-11FSNV991_Affected_CPN_11132020.csv](#)

Notification Text:

SYST-11FSNV991

Microchip has released a new Product Documents for the ATSAMHAXGXXA Data Sheet of devices. If you are using one of these devices please read the document located at [ATSAMHAXGXXA Data Sheet](#)

Notification Status:

Final

Description of Change:

- 1) Fixing ordering codes for SAMHA0 variants
- 2) Updated the I/O Multiplexing and Considerations chapter
- 3) Added chapter Register Access and Behavior
- 4) Added IOBUS to the Physical memory map chapter
- 5) Updated the Generic Clock Generator Division chapter, clarifying the use of division factor bits and adding missing Write-Synchronized property
- 6) Updated Generate Clock Selection ID tables in the 17. GCLK – Generic Clock Controller chapter, fixing erroneous entries
- 7) Updated the DAC INTENSET chapter, clarifying the description of the SYNCRDY bit
- 8) Added the DID values in the 15.10 Device Identification chapter
- 9) Clarified the Reset values in chapter DSU.STATUSUSB
- 10) Clarified description for the GAIN bits in the SYSCTRL.XOSC register

- 11) Fixed available MUXPOS and MUXPEG options in chapter INPUTCTRL
- 12) Removed references to settings for BOD12. Not meant to be modified by the user
- 13) Added note clarifying the mode transitions
- 14) Dynamic max ratings of pin VS updated in Table Max ratings
- 15) Clarified Usage of DMAC in Sleep mode operation
- 16) Fixed wrong info in the bit description for DMA TRIGSRC
- 17) Removed references to external interrupts that are not present on this device
- 18) Fixed information about 26.6.2.7 Event Generators
- 19) Fixed TC Pairing info for COUNT32 mode
- 20) Added Reset schematic diagram
- 21) Fixed conditions for parameter 10.10 in the electrical characteristics for the SBC
- 22) The errata section has been moved to a separate document that can be found on the product webpage
- 23) Minor editorial changes

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 13 Nov 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[ATSAMHAXGXXA Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to [receive Microchip PCNs via email](#) please register for our PCN email service at our [PCN home page](#) select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the [PCN FAQ](#) section.

If you wish to [change your PCN profile, including opt out](#), please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.