



## Product Change Notification: SYST-10UAEP996

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**Date:**

11-Jun-2025

**Product Category:**

32-Bit Microprocessors

**Notification Subject:**

Data Sheet - SAMA7G5 Series System-in-Package (SiP) MPUs with Embedded DDR3L SDRAM

**Affected CPNs:**

[SYST-10UAEP996\\_Affected\\_CPN\\_06112025.pdf](#)

[SYST-10UAEP996\\_Affected\\_CPN\\_06112025.csv](#)

**Notification Text:**

SYST-10UAEP996
<p>Microchip has released a new Datasheet for the SAMA7G5 Series System-in-Package (SiP) MPUs with Embedded DDR3L SDRAM of devices. If you are using one of these devices please read the document located at <a href="#">SAMA7G5 Series System-in-Package (SiP) MPUs with Embedded DDR3L SDRAM</a>.</p> <p><b>Notification Status:</b> Final</p> <p><b>Description of Change:</b> Throughout: added content related to SAMA7G54D4G(T)-I/4UB(-SLx)</p> <p><b>Impacts to Data Sheet:</b> See above details.</p> <p><b>Reason for Change:</b> To improve productivity.</p> <p><b>Change Implementation Status:</b> Complete</p> <p><b>Date Document Changes Effective:</b> 11 Jun 2025</p> <p><b>NOTE:</b> Please be advised that this is a change to the document only the product has not been</p>

**Description of Change:**

Throughout: added content related to SAMA7G54D4G(T)-I/4UB(-SLx)

**Impacts to Data Sheet:** See above details.

**Reason for Change:** To improve productivity.

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 11 Jun 2025

**NOTE:** Please be advised that this is a change to the document only the product has not been

changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## **Attachments:**

### **SAMA7G5 Series System-in-Package (SiP) MPUs with Embedded DDR3L SDRAM**

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

## **Terms and Conditions:**

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