



## Product Change Notification: SYST-08QNK048

**Date:**

09-Oct-2025

**Product Category:**

Clock Buffers

**Notification Subject:**

Data Sheet - SY75608/612 - 8 or 12 Output Ultra-Low Additive Jitter PCI Express Clock Buffer

**Affected CPNs:**

[SYST-08QNK048\\_Affected\\_CPN\\_10092025.pdf](#)

[SYST-08QNK048\\_Affected\\_CPN\\_10092025.csv](#)

**Notification Text:**

SYST-08QNK048

Microchip has released a new Datasheet for the SY75608/612 - 8 or 12 Output Ultra-Low Additive Jitter PCI Express Clock Buffer of devices. If you are using one of these devices please read the document located at [SY75608/612 - 8 or 12 Output Ultra-Low Additive Jitter PCI Express Clock Buffer](#).

**Notification Status:** Final

**Description of Change:**

- Updated Features and General Description with PCIe Gen 7 support information.
- Added PCIe Gen 7 performance information and values to the Jitter and Phase Noise table.
- Updated Functional Description with PCIe Gen 7 support information.

**Impacts to Data Sheet:** See above details.

**Reason for Change:** To improve productivity.

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 09 Oct 2025

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## **Attachments:**

**SY75608/612 - 8 or 12 Output Ultra-Low Additive Jitter PCI Express Clock Buffer**

Please contact your local [\*\*Microchip sales office\*\*](#) with questions or concerns regarding this notification.

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