



Product Change Notification: SYST-07EXFM001

Date:

11-Nov-2025

Product Category:

Interface- Serial Peripherals

Notification Subject:

Data Sheet - MCP23008 and MCP23S08 Data Sheet

Affected CPNs:

[SYST-07EXFM001_Affected_CPN_11112025.pdf](#)

[SYST-07EXFM001_Affected_CPN_11112025.csv](#)

Notification Text:

SYST-07EXFM001

Microchip has released a new Datasheet for the MCP23008 and MCP23S08 Data Sheet of devices. If you are using one of these devices please read the document located at [MCP23008 and MCP23S08 Data Sheet](#).

Notification Status: Final

Description of Change:

APPENDIX A: REVISION HISTORY

Revision H (November 2025)

- Corrected drawings for MCP23008 (QFN only)

and MCP23S008 (QFN only) in Section

Package Types.

- Updated Package Outline Drawing for the SOIC

package in Section 3.0 “Packaging

Information”.

Revision G (June 2022)

- Updated description in Section “Features”.
- Updated Table 1-1.
- Corrected drawings for MCP23008 in Section Package Types.
- Corrected Section 1.6.1 “I/O Direction (IODIR) Register”
- Corrected packages drawings in Section 3.0 “Packaging Information”.
- Updated Section “Product Identification System”, with Automotive Qualified devices.
- Minor text and format changes throughout.

Revision F (March 2019)

- Updated the QFN package drawing in Section Package Types.
- Updated Section 3.0 “Packaging Information”.
- Minor typographical edits.

Revision E (August 2007)

- Section 3.0 “Packaging Information”: Updated package outline drawings.

Revision D (February 2007)

- Changed Byte and Sequential Read in Figure 1-1 from “R” to “W”.

- Table 2-4, Param No. 51 and 53: Changed from 450 to 600 and 500 to 600, respectively.
- Added disclaimer to package outline drawings.
- Updated package outline drawings.

Revision C (October 2006)

- Added 20-pin QFN package information throughout document.
- Added disclaimer to package outline drawings.

Revision B (February 2005)

- Section 1.6 “Configuration and Control Registers”. Added Hardware Address Enable (HAEN) bit to Table 1-3.
- Section 1.6.6 “Configuration (IOCON) Register”. Added Hardware Address Enable (HAEN) bit to Register 1-6.

Revision A (December 2004)

- Original release of this document.

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 11 Nov 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:**MCP23008 and MCP23S08 Data Sheet**

Please contact your local [**Microchip sales office**](#) with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to receive Microchip PCNs via email please register for our PCN email service at our [**PCN home page**](#) select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the [**PCN FAQ**](#) section.

If you wish to change your PCN profile, including opt out, please go to the [**PCN home page**](#) select login and sign into your myMicrochip account. Select a profile option from