



Product Change Notification: SYST-06WBWL013

Date:

12-Aug-2025

Product Category:

Clock And Timing - Clock Generation

Notification Subject:

Data Sheet - ZL30264-67 - 2 APLL, 6- or 10-Output Any-to-Any Clock Multiplier and Frequency Synthesizer

Affected CPNs:

[SYST-06WBWL013_Affected_CPN_08122025.pdf](#)
[SYST-06WBWL013_Affected_CPN_08122025.csv](#)

Notification Text:

SYST-06WBWL013

Microchip has released a new Datasheet for the ZL30264-67 - 2 APLL, 6- or 10-Output Any-to-Any Clock Multiplier and Frequency Synthesizer of devices. If you are using one of these devices please read the document located at [ZL30264-67 - 2 APLL, 6- or 10-Output Any-to-Any Clock Multiplier and Frequency Synthesizer](#).

Notification Status: Final

Description of Change:

General Features: Updated PCIe compliance bullet to include Gen 7.0.
Output Clock Features: Updated PCIe compliance bullet to include Gen 7.0.
Table 7-12: Added values/info for PCIe Gen 7.0.

Impacts to Data Sheet: See above details.

Reason for Change: To improve productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 12 Aug 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL30264-67 - 2 APLL, 6- or 10-Output Any-to-Any Clock Multiplier and Frequency Synthesizer

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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