Product Change Notification: SYST-06TDNQ016

Date:

13-Nov-2026

Product Category:

Wireless IC, Wireless Modules

Notification Subject:

PIC32 BZ6 Family Silicon Errata

Affected CPNs:

SYST-06TDNQ016_Affected_CPN_11132026.pdf
SYST-06TDNQ016_Affected_CPN_11132026.csv

Notification Text:

SYST-06TDNQ016

Microchip has released a new Document for the PIC32 BZ6 Family Silicon Errata of devices. If you are using one of these devices please read the document located at **PIC32 BZ6 Family Silicon Errata**.

Notification Status: Final

Description of Change:

Revision E, 11/2026

Updated Silicon Errata Summary and Silicon Errata Issues.

- Updated “Configurable Custom Logic” to “Analog Comparators (AC)”.
- Added the following erratum:

1. Analog Comparators (AC)
   a. Improper Comparison Operation in Standby Sleep Mode
2. General-Purpose Input/Output (GPIO)
   a. GPIO Change Notification Interrupt not Firing After Sleep
3. Quadrature Encoder Interface (QEI)
   a. Interval Timer (INTTMRx) Does Not Clear on Invalid QEI State Transition
4. Root of Trust (ROT)
   a. Storage Root Key (SRK) Not Supported
5. Serial Communication Interface (SERCOM)
   a. SERCOM-SPI: SPI Host Mode
   b. SERCOM-I2C Host Mode: Second DMA Packet Corrupted When LENEN = 1

- Updated Run Mode Watchdog Counter is Not Cleared Before Standby Sleep Instruction

Impacts to Data Sheet: None

Reason For Change: To Improve Productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 13 Nov 2026

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

PIC32 BZ6 Family Silicon Errata

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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