



Product Change Notification: SYST-05ZGIF502

Date:

09-Jan-2026

Product Category:

Wireless IC, Wireless Modules

Notification Subject:

Data Sheet - Highly Integrated Wireless MCU with CAN FD, Ethernet, USB, Motor Control, Graphics, Touch and Enhanced Security

Affected CPNs:

[SYST-05ZGIF502_Affected_CPN_01092026.pdf](#)

[SYST-05ZGIF502_Affected_CPN_01092026.csv](#)

Notification Text:

SYST-05ZGIF502

Microchip has released a new Datasheet for the Highly Integrated Wireless MCU with CAN FD, Ethernet, USB, Motor Control, Graphics, Touch and Enhanced Security of devices. If you are using one of these devices please read the document located at [Highly Integrated Wireless MCU with CAN FD, Ethernet, USB, Motor Control, Graphics, Touch and Enhanced Security](#).

Notification Status: Final

Description of Change:

- Appendix B: Acronyms and Abbreviations - Updated Table 52-1
- PIC32CX2051BZ66048 SoC Packaging Dimension and PIC32CX2051BZ66048 SoC Package Marking - Added section
- PIC32CX2051BZ62132 SoC Package Marking and PIC32WM-BZ6204 Module Packaging Dimension - Updated section
- IEEE 802.15.4 RF Characteristics - Updated section and updated figure titles
- XOSC32 (SOSC) AC Electrical Specifications - Updated Table 49-15
- I/O Pin AC/DC Electrical Specifications - Updated DI_19 and DI_21 parameters in Table 49-12

- Wake-Up Timing from Low Power Modes AC Electrical Specifications - Updated WKUP_13 and WKUP_15 parameters in Table 49-11
- Power Supply DC Module Electrical Specifications - Updated Table 49-5
- Thermal Specifications - Updated Table 49-4
- Absolute Maximum Ratings - Updated Table 49-1
- Overview, Features, Features, Low-Cost Controllerless (LCC) - Updated section with PIC32CX2051BZ66048 SoC information
- I/O Lines - Updated Table 35-2
- Register Summary - Updated section with 48-pin SoC information
- CFGCON1(L) - Updated SERCOM_HSEN, Added Note, Bit 14 updated to MCLR
- VDD Voltage Domain Overview - Updated section
- Block Diagram - Updated section (FAVdd) and updated Figure 8-1
- Pinout and Signal Descriptions List - Added Table 6-2
- Power Pins - Updated Figure 5-3
- Pinout Diagram - Added Figure 4-3 and Figure 4-4
- PIC32CX2051BZ62132 SoC Block Diagram - Updated section
- PIC32CX2051BZ66048 SoC Block Diagram - Added section
- PIC32CX-BZ6 SoC Description - Updated chapter
- Basic Connection Requirement - Updated Figure 5-2
- PIC32WM-BZ6 Module Description - Updated Figure 5-1
- Configuration Summary - Updated Table 3-1, Table 3-2 and Table 3-3
- PIC32WM-BZ6 Module Ordering Information - Updated Table 2-2
- PIC32CX-BZ6 SoC Ordering Information - Updated Table 2-1, updated Figure 2-1, and added 48-pin SoC details

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 09 Jan 2026

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

Highly Integrated Wireless MCU with CAN FD, Ethernet, USB, Motor Control, Graphics, Touch and Enhanced Security

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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