



Product Change Notification: SYST-05TICN131

Date:

20-Oct-2025

Product Category:

Wireless IC, Wireless Modules

Notification Subject:

Data Sheet - PIC32CX-BZ6 and PIC32WM-BZ6 Family Data Sheet

Affected CPNs:

[SYST-05TICN131_Affected_CPN_10202025.pdf](#)

[SYST-05TICN131_Affected_CPN_10202025.csv](#)

Notification Text:

SYST-05TICN131

Microchip has released a new Datasheet for the PIC32CX-BZ6 and PIC32WM-BZ6 Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32CX-BZ6 and PIC32WM-BZ6 Family Data Sheet](#).

Notification Status: Final

Description of Change:

Section	Description
• Introduction	Updated section
• PIC32CX-BZ6 SoC Family Features	
• PIC32WM-BZ6 Module Features	
Added section	
Updated section	
Updated Figure 5-1	

Updated Figure 5-2	
Updated Figure 5-4	
Updated section	
Added Figure 5-5	
Updated Table 6-1 with Note 11.	
Updated section, ADC module to ADC core	
Updated Table 7-5	
Updated Table 7-15	
NVMPREPG, updated Bit 0 description	
Updated Figure 18-1	
Added Note	
Updated Deep Sleep Mode Entry section	
Updated section	
Section added	
Register added	
• Updated Table 24-1	
• Updated PMD1 register	
Added Note information to the PBDB bit	
Updated section	
Added Note information to the ENABLE bit	
Added Figure 29-9	
DONE, updated short description	
Updated section	
Updated ADC Module with ADC core in the entire chapter	
• Block Diagram	Added Note
• ADC Operation	
Updated section	

Updated Code Example for ADC Class 2 Configuration and Fractional Format	
Updated ADC Scanning Multiple Inputs Code Example	
Updated Table 37-4	
Updated register	
Updated Note 1 and 2	
Updated Note	
Added Note 3.	
SWRST, added Note information	
WSTATE0, added Note information	
Updated Note	
• Updated section	
• Added Note information	
• Updated section	
• Updated SWRST with Note information	
Updated CMD bit-field description	
Updated MCx bit-field description	
Added Note to Standard RAMP2 (RAMP2) Operation	
Updated DIR bitfield description	
Updated MCx bit-field description	
Adding Note information to RAMP bit	
Adding Note information to the registers	
Updated section	
Updating register reset value	
Updated section	
Updated Figure 44-1	
Updated section	

Updated MAG bitfield description	
Updated Bit [13:10] bitfield register descriptions	
Added section	
DC Electrical Characteristics	Updated DC_5, DC_6 and DC_7 parameters in Table 49-2
Updated Table 49-4	
Updated Table 49-5	
Updated Table 49-6	
Updated Table 49-7	
Updated Table 49-8	
Updated Table 49-9	
Updated Table 49-10	
Updated section	
Updated Table 49-12	
Updated Table 49-13	
Updated XOSC_1 parameter in Table 49-14	
Updated Table 49-15	
Updated Table 49-16	
Updated Table 49-17	
Updated DAC Module Electrical Specifications	
Updated Table 49-22, Table 49-23, Table 49-24 and	
Table 49-25	
Updated Table 49-26	
Updated Figure 49-20 and Figure 49-21	
Updated Table 49-29	
• Updated Table 49-30 and Table 49-31	
• Updated Figure 49-22, Figure 49-23, Figure 49-24	

and Figure 49-25	
Updated Table 49-32 and Table 49-33	
Added Figure 49-26	
Updated Table 49-37	
Updated Table 49-39	
• Updated section	
• Figure 49-32	
• Figure 49-33	
• Updated the section to include performance graphs for Bluetooth Low Energy under various operating conditions.	
• Updated Table 49-43	
802.15.4 RF Characteristics	• Updated the section to include performance graphs for Zigbee under various operating conditions.
	• Updated Table 49-45
Updated DI_9 VOH 12x Drive Strength I/O pins (Output High) VDDIO = 3.3V at IOH value.	
Updated with latest drawings	
Added Regulatory Approval details	

Impacts to Data Sheet: See above details

Reason for Change: To improve productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 20 Oct 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

PIC32CX-BZ6 and PIC32WM-BZ6 Family Data Sheet

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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