Product Change Notification / SYST-03QQZX502

Date:

04-Feb-2021

Product Category:

Clock and Timing - Clock and Data Distribution

PCN Type:

Document Change

Notification Subject:

Data Sheet - ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control

Affected CPNs:

SYST-03QQZX502_Affected_CPN_02042021.pdf
SYST-03QQZX502_Affected_CPN_02042021.csv

Notification Text:

SYST-03QQZX502

Microchip has released a new Product Documents for the ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control of devices. If you are using one of these devices please read the document located atZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control

Notification Status: Final

Description of Change:
1) Updated Pin Names for Pins 42 and 43.

Impacts to Data Sheet: None
Reason for Change: To Improve Manufacturability
Change Implementation Status: Complete

Date Document Changes Effective: 04 Feb 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.
Markets to Distinguish Revised from Unrevised Devices: N/A

Attachments:

ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control

Please contact your local Microchip sales office with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to receive Microchip PCNs via email please register for our PCN email service at our PCN home page select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the PCN FAQ section.

If you wish to change your PCN profile, including opt out, please go to the PCN home page select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.