



Product Change Notification: SYST-02GFQQ955

Date:

09-Dec-2025

Product Category:

32-Bit Microprocessors

Notification Subject:

Data Sheet - SAMA7D6 Series Data Sheet

Affected CPNs:

[SYST-02GFQQ955_Affected_CPN_12092025.pdf](#)

[SYST-02GFQQ955_Affected_CPN_12092025.csv](#)

Notification Text:

SYST-02GFQQ955

Microchip has released a new Datasheet for the SAMA7D6 Series Data Sheet of devices. If you are using one of these devices please read the document located at [SAMA7D6 Series Data Sheet](#).

Notification Status: Final

Description of Change:

General

Added content related to the SAMA7D65(T)-V/7EW(-SLx) device

Debug and Test

JTAG Debug Port (JTAG-DP), Serial Wire Debug Port (SW-DP): updated VERSION and DESIGNER descriptions

Boot Strategies

Updated Description, Secure Boot Configuration Packet

Static Memory Controller (SMC)

HSMC_SETUPx, HSMC_PULSEx: corrected register access to "Read/Write"

DDR/LPDDR Physical Interface

(DDR3PHY) Updated DDR3PHY_MR_x register names

Real-Time Clock (RTC)

Updated UTC Mode

RTC_MR: updated the UTC and HRMODE descriptions

RTC_TIMALR (DEFAULT_MODE): updated the AMPM bit description

Advanced Encryption Standard (AES)

Updated Start Modes, Encrypted Tweak Generation, Data Processing

XEX-based Tweaked-codebook Mode (XTS): updated auto-padding information

Flexible Serial Communication Controller (FLEXCOM)

Modified content regarding multiple data reading in SPI Multiple Data Access,

FLEX_SPI_RDR (FIFO_MULTI_DATA_8), FLEX_SPI_RDR (FIFO_MULTI_DATA_16)

FLEX_SPI_RDR (FIFO_MULTI_DATA_8), FLEX_SPI_RDR (FIFO_MULTI_DATA_16), FLEX_SPI_TDR

(FIFO_MULTI_DATA): “FIFO” removed from register name (but kept in register acronym)

Secure Digital MultiMedia Card Controller (SDMMC)

SDMMC_CALCR: updated CLKDIV description

SDMMC SDMA System Address/Argument 2 Register split into SDMMC_SSAR

(DEFAULT_MODE) and SDMMC_SSAR (CMD23)

Electrical Characteristics

Table 85.4: updated VDDCPU conditions

Table 85.6: updated fCPU_CLK conditions

Table 85.76: updated Use Case 6 values

Reworked Flexible Serial Communication Controller (FLEXCOM) - USART

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 09 Dec 2025

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

SAMA7D6 Series Data Sheet

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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