



Product Change Notification / SYST-02DOZZ491

Date:

10-Mar-2023

Product Category:

General Purpose FPGAs, System On Chip FPGAs

PCN Type:

Document Change

Notification Subject:

Data Sheet - SmartFusion2 Microcontroller Subsystem User Guide

Affected CPNs:

[SYST-02DOZZ491_Affected_CPN_03102023.pdf](#)

[SYST-02DOZZ491_Affected_CPN_03102023.csv](#)

Notification Text:

SYST-02DOZZ491

Microchip has released a new Datasheet for the SmartFusion2 Microcontroller Subsystem User Guide of devices. If you are using one of these devices please read the document located at [SmartFusion2 Microcontroller Subsystem User Guide](#).

Notification Status: Final

Description of Change:

- Remapping eNVM data from eNVM_1 memory block to Cortex®-M3 Code space is not permitted for SmartFusion® 2 M2S090/150 and IGLOO® 2 M2GL090/150 devices. For information about eNVM remapping and limitation, see the note under Figure 4-28.
- Timing models for Fabric to MSS interrupts have been updated with additional time delay. This changes the timing arcs of nets and interface between Fabric to MSS interrupts. For more information about the updated timing arcs, see PCN 17005A.
- Updated 1.5.2.3. Embedded Trace Macrocell to include information about timing arcs update from Fabric to Embedded Trace Macrocell.
- Updated 10.5.1. SGMII Interface Configuration to include information about timing arcs update from SerDes to Fabric.
- Updated 22.3.1. Configuring the FIIC Using the Libero SoC to include information about timing arcs update from Fabric to MSS interrupts.
- The document was converted to Microchip template.
- The document number was changed to DS50003495 from UG0331.

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 10 Mar 2023

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SmartFusion2 Microcontroller Subsystem User Guide](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to receive Microchip PCNs via email please register for our PCN email service at our [PCN home page](#) select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the [PCN FAQ](#) section.

If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.