



Product Change Notification / RMES-20TRPU236

Date:

21-Feb-2022

Product Category:

FPGA Software Licenses

PCN Type:

Manufacturing Change

Notification Subject:

eSign # E000090941 Final Notice: Release of Libero SoC 2022.1 for selected products in the PolarFire FPGA device family.

Affected CPNs:

[RMES-20TRPU236_Affected_CPN_02212022.pdf](#)
[RMES-20TRPU236_Affected_CPN_02212022.csv](#)

Notification Text:

PCN Status:Final Notification

PCN Type:Manufacturing Change

Microchip Parts Affected:Please open one of the files found in the Affected CPNs section.
Note: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change:Release of Libero SoC 2022.1 for selected products in the PolarFire FPGA device family. [Customer notification details](#).

Note:Once released, Libero SoC v2022.1 will be available for download on the webpage below.

<https://www.microchip.com/en-us/products/fpgas-and-plds/fpga-and-soc-design-tools/fpga/libero-software-later-versions#Documents%20and%20Downloads>

Pre and Post Change Summary:

	Pre Change	Post Change
Firmware	The PCIe Sub-system (PCIESS) LTSSM supports Link Training states and Re-Training (Recovery) state. However, the PCIESS does not support any software driven power management states such as L2P2, as incorrectly noted in the original documentation.	Software-driven power management states such as L2P2 in PolarFire PCIESS Root-Port and Endpoints are not supported.

Impacts to Data Sheet:

None

Change ImpactNone

Reason for Change: Software-driven PCIe L2P2 entry commands issued by the PolarFire PCIESS Root-Port to the downstream end-points are not supported. As a root-port, this will cause the link to be completely disrupted and only recoverable by re-initializing the link with side-band PERSTn (fundamental reset) or a power cycle.

PolarFire PCIESS End-point should not be commanded by the host to enter L2P2 link state. As an end-point, the link may be disruptive and only recoverable by re-initializing the link with side-band PERSTn (fundamental reset) or a power cycle.

Change Implementation Status: In Progress

Estimated First Ship Date: April 06, 2022

Note: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Time Table Summary:

	February 2022				>	April 2022				
	06	07	08	09		1 4	1 5	1 6	1 7	1 8
Final PCN Issue Date				x						
Estimated Implementation Date						x				

Method to Identify Change: Not applicable. New software will be available as defined above.

Qualification Report: Not Applicable

Revision History:

February 21, 2022: Issued final notification to reveal the appropriate software release.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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