



Product Change Notification

Product Change Notification Number: WC124806

Notification Date: February 11, 2013

Title: Die Revision Change for ATxmega64A3U									
Product Identification:									
<table border="1"><tr><td>ATxmega64A3U-AU</td><td>ATxmega64A3U-AUR</td></tr><tr><td>ATxmega64A3U-MH</td><td>ATxmega64A3U-MHR</td></tr></table>		ATxmega64A3U-AU	ATxmega64A3U-AUR	ATxmega64A3U-MH	ATxmega64A3U-MHR				
ATxmega64A3U-AU	ATxmega64A3U-AUR								
ATxmega64A3U-MH	ATxmega64A3U-MHR								
Reason for Change:	<input checked="" type="checkbox"/> Material / Composition <input type="checkbox"/> Processing / Manufacturing	<input checked="" type="checkbox"/> Design / Firmware <input type="checkbox"/> Logistics	<input type="checkbox"/> Manufacturing Location <input checked="" type="checkbox"/> Quality / Reliability						
Change Description: This notification is to advise our customers that Atmel will introduce new revisions of the AVR microcontroller products listed above. The new revisions are package and pin compatible to the existing revisions. They are introduced in order to enhance the product. Actual devices changes are minimal, but for your reference they are listed here together with enhancements that are a pure superset of existing functions. Samples are only available in bulk and can be ordered through Atmel Sample Centre by logging on to https://samples.atmel.com/scripts/samplecenter.dll?atmel?cmd=menu Specific ordering codes for new die revision samples only are shown in the table below, and are available for sample orders only until the proposed first shipment date. For all production orders, only standard existing ordering codes will be accepted.									
<table border="1"><thead><tr><th>Part number</th><th>Ordering code for samples</th></tr></thead><tbody><tr><td>ATxmega64A3U-AU</td><td>ATxmega64A3U-AUK</td></tr><tr><td>ATxmega64A3U-MH</td><td>ATxmega64A3U-MHK</td></tr></tbody></table>		Part number	Ordering code for samples	ATxmega64A3U-AU	ATxmega64A3U-AUK	ATxmega64A3U-MH	ATxmega64A3U-MHK		
Part number	Ordering code for samples								
ATxmega64A3U-AU	ATxmega64A3U-AUK								
ATxmega64A3U-MH	ATxmega64A3U-MHK								
<u>Note that the K in sample ordering codes will not be marked on the package.</u>									
Changes The new revision change the following: <ul style="list-style-type: none">• Reduced current consumption• Chip erase time during programming reduced• Bonding wire material has changed from gold to copper• The I/O pins comply with the JEDEC LVTTL and LVCMS specification and the high- and low level input and output voltage limits reflect or exceed this specification.									
See Appendix 1 for more details on changes.									
Identification Method to Distinguish Change: For packages where space allows for die ID to be part of marking, new revision material is identified as 35973I.									
Qualification Data:	<input checked="" type="checkbox"/> Available	<input type="checkbox"/> Will be available (mm/dd/yr):	<input type="checkbox"/> Not Applicable						
Samples:	<input checked="" type="checkbox"/> Available	<input type="checkbox"/> Will be available (mm/dd/yr):	<input type="checkbox"/> Not Applicable						

Quantifiable Impact on Quality & Reliability:

None

Proposed First Ship Date*: May 8, 2013

**The Proposed First Ship Date is the forecasted date that a customer may expect to receive changed product. This is determined by the estimated date of inventory depletion on the PCN issue date. This may be affected by fluctuations in supply and demand. Consequently, although customers should be prepared to receive changed product on this date, Atmel will continue to ship pre-changed product until a time in which inventory has been depleted. This may result in pre-changed product being shipped to customers after this forecasted date.*

Atmel Contact: Please contact your Atmel Sales Representative or Distributor for additional information (when replying via e-mail please include PCN number in subject line).

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CUSTOMER ACKNOWLEDGEMENT OF RECEIPT: Atmel requests you acknowledge receipt of this PCN.

Please complete and email to pcnadm@atmel.com and the Atmel Contact listed above. In your acknowledgement, you can grant approval or request additional information. **Atmel will deem this change accepted unless specific conditions of acceptance are provided in writing within 30 days from the date of this notice.**

Company:	
Name:	
Title:	
Date:	
Email Address:	
Location:	
Comments:	

Appendix 1: Change Description Details

Reduced current consumption

The table below lists the typical and maximum current consumption in the existing and new revision.

Parameter	Condition	Existing revisions			New revision			Units
		Min	Typ	Max	Min	Typ	Max	
Active power consumption	32kHz, Ext. Clk	Vcc = 1.8V		120			50	µA
		Vcc = 3.0V		270			125	
	1MHz, Ext. Clk	Vcc = 1.8V		350			250	
		Vcc = 3.0V		697			520	mA
	2MHz, Ext. Clk	Vcc = 1.8V		658	700		450	
		Vcc = 3.0V		1.1	1.4		0.9	
Idle power consumption	32MHz, Ext. Clk			10.6	15		9.5	15
	32kHz, Ext. Clk	Vcc = 1.8V		4.3			3.0	µA
		Vcc = 3.0V		4.8			4.8	
	1MHz, Ext. Clk	Vcc = 1.8V		78			75	
		Vcc = 3.0V		150			140	mA
	2MHz, Ext. Clk	Vcc = 1.8V		150	350		145	
Power-down power consumption	T = 25°C	Vcc = 3.0V		290	600		275	450
	T = 85°C			4.7	7.0		4.4	7.0
	WDT and sampled BOD enabled, T = 25°C			0.1	1.0		0.1	1.0
				1.8	5.0		1.6	5.0
	WDT and sampled BOD enabled, T = 85°C			1.3	3.0		1.3	3.0
Power-save power consumption	RTC from ULP clock, WDT and sampled BOD enabled, T = 25°C	Vcc = 1.8V		3.1	7.0		2.7	7.0
		Vcc = 3.0V		1.2			1.2	µA
	RTC from 1.024kHz low power 32.768kHz TOSC, T = 25°C	Vcc = 1.8V		0.6	2.0		0.6	
		Vcc = 3.0V		0.7	2.0		0.7	
	RTC from low power 32.768kHz TOSC, T = 25°C	Vcc = 1.8V		0.8	3.0		0.8	3.0
Reset power consumption	Current through RESET pin substracted	Vcc = 3.0V		250			150	

Chip erase time during programming

For the existing revisions the chip erase time is about 105ms, while in the new revision this is lowered to 55ms.