

PCN Number: RC134001
Notification Date*: November 5, 2013

Title: Migration to Marketing Revision Level C of the AT91SAM7X128 and AT91SAM7X256

Product Identification:

Current Part Number	New Part Number
AT91SAM7X256B-AU	AT91SAM7X256C-AU
AT91SAM7X256B-AU-999	AT91SAM7X256C-AUR
AT91SAM7X256B-CU	AT91SAM7X256C-CU
AT91SAM7X256B-CU-999	AT91SAM7X256C-CUR
AT91SAM7X128B-AU	AT91SAM7X128C-AU
AT91SAM7X128B-AU-999	AT91SAM7X128C-AUR
AT91SAM7X128B-CU	AT91SAM7X128C-CU
AT91SAM7X128B-AU-999	AT91SAM7X128C-CUR

Reason for Change:

- ☐ Material / Composition
☒ Processing / Manufacturing
☒ Design / Firmware
☒ Datasheet

- ☐ Manufacturing Location
☐ Quality / Reliability
☐ Logistics
☐ Other:

Change Description:

A complete list of differences between the marketing revision level (MRL) B and C can be found in chapter 41 of the AT91SAM7X series datasheet with Atmel document reference 6120I. Multiple errata have been corrected on the ADC, CAN, EMAC, PWM, SPI, SSC, TWI and USART.

The ChipID has been incremented to:

AT91SAM7X256 Revision C chip ID is 0x275B 0942

AT91SAM7X128 Revision C chip ID is 0x275A 0742

The Flash memory access time has been reduced as per the below table:

Flash Wait State	Read Operations	Maximum Operating Frequency (MHz)
0	1 cycle	20
1	2 cycles	48.1
2	3 cycles	55

Migration from the current 0.18µm process to the 0.15µm process.

Identification Method to Distinguish Change:

Marketing Revision Level (MRL) has been incremented on the top side marking to C.

Qualification Data:
☒ Available

☐ Will be available
(mm/dd/yr):

☐ Not Applicable

Samples:	☒ Available	☐ Will be available (mm/dd/yr):	☐ Not Applicable
Quantifiable Impact on Quality & Reliability: None			
Forecasted Availability Date: September 30, 2013 Last Time Buy Date: January 8, 2014 Last Ship Date: October 31, 2014 <i>*All orders placed after the notification date are non-cancellable and non-returnable (NCNR).</i>			
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