Product/Process Change Notice - PCN 24_0198 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title:	AD8410A/AD8411A Data Sheet Revision
Publication Date:	23-Sep-2024
Effectivity Date:	26-Dec-2024 *(the earliest date that a customer could expect to receive changed material)*
Revision Description:	Initial Release.

Description Of Change:

Data Sheet correction on Table 1. Electrical Specifications:
1. Input Bias Current Line Specification changed to 350uA. See attached Data Sheet comparison document in the Supporting Documents section of this PCN.

Reason For Change:

Data sheet indicates Total Input-Bias Current which is the sum of two input bias current values. Currently, the data sheet reflects ibias current per pin.

Impact of the change (positive or negative) on fit, form, function & reliability:

No impact on fit, form, function and reliability when operated within data sheet specifications. There are no changes to bond pad locations and bonding diagram.

Summary of Supporting Information:

Data Sheet specification change will be reflected in revision B of the Product Data Sheet. See attached Data Sheet Comparison.

Supporting Documents

Attachment 1: Type: Datasheet Specification Comparison

ADI_PCN_24_0198_Rev_-_AD8410A-AD8411A Datasheet Change (Ibias Line Spe...

Note: If applicable, the device material declaration will be updated due to material change.

ADI Contact Information:

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:	Europe:	Japan:	Korea:	Rest of Asia:
PCN_Americas@analog.com	PCN_Europe@analog.com	PCN_Japan@analog.com	PCN_Korea@analog.com	PCN_ROA@analog.com

Appendix A - Affected ADI Models:

Added Parts On This Revision - Product Family / Model Number (8)

AD8410A / AD8410AWBRMZ	AD8410A / AD8410AWBRMZ-RL	AD8410A / AD8410AWBRZ	AD8410A / AD8410AWBRZ-RL	AD8411A / AD8411AWBRMZ
AD8411A / AD8411AWBRMZ-RL	AD8411A / AD8411AWBRZ	AD8411A / AD8411AWBRZ-RL		

Appendix B - Revision History:			
Rev	**Publish Date**	**Effectivity Date**	**Rev Description**
Rev. -	23-Sep-2024	26-Dec-2024	Initial Release.

ANALOG DEVICES

AHEAD OF WHAT'S POSSIBLE™

AD8410A-AD8411A Datasheet Change (Ibias Line Spec)

August 2024

AD8410A DS Change on Ibias Line Spec

SPECIFICATIONS

T_A = −40°C to +125°C (operating temperature range), supply voltage (V_S) = 5 V, ground (GND) = 0 V, Input Common-Mode Voltage (VCM) = -IN, +IN =12 V, and $V_{REF}1 = V_{REF}2$ = 2.5 V, unless otherwise noted.

Table 1. Electrical Specifications

Parameter	Test Conditions/Comments	Min	Typ	Max	Unit
GAIN					
Initial			20		V/V
Error Over Temperature	Specified temperature range			0.13	%
Gain vs. Temperature				±6	ppm/°C
VOLTAGE-OFFSET	Referred to input (RTI)				
Over Temperature	Specified temperature range[1]			±200	µV
Offset Drift	Box method (see Figure 56)		±0.21	±0.71	µV/°C
	Bowtie method (-40°C to 25°C) (see Figure 57)			±1.84	µV/°C
	Bowtie method (25°C to 125°C) (see Figure 57)			±1.51	µV/°C
INPUT					
Total Input-Bias Current[2]	+IN = −IN = 0 V, $V_S = V_{REF}1$ = 5 V, $V_{REF}2$ = 0 V	−10.0			µA
	+IN = −IN = 12 V, $V_S = V_{REF}1 = V_{REF}2$ = 0 V, T_A = 25°C		44		µA
	+IN = −IN = 12 V, $V_S = V_{REF}1$ = 5 V, $V_{REF}2$ = 0 V			175	µA
	+IN = −IN = 48 V, $V_S = V_{REF}1 = V_{REF}2$ = 0 V, T_A = 25°C		178		µA
	+IN = −IN = 48 V, $V_S = V_{REF}1$ = 5 V, $V_{REF}2$ = 0 V			484	µA
Input Offset Current	+IN = −IN = 0 V			1.0	µA
	+IN = −IN = 12 V			2.5	µA
	+IN = −IN = 48 V			2.7	µA
Input Voltage Range	Common mode, continuous	−2		+70	V
Common-Mode Rejection Ratio (CMRR)	Specified temperature range, DC, V_{CM} = −2 V to +70 V	123	142		dB
	T_A = 25°C, frequency = 10 kHz		110		dB
	T_A = 25°C, frequency = 50 kHz		96		dB

Change to 350uA

AD8411A DS Change on Ibias Line Spec

Data Sheet AD8411A

SPECIFICATIONS

T_A = −40°C to +125°C (Operating Temperature Range), supply voltage (V_S) = 5 V, ground (GND) = 0 V, Input Common-Mode Voltage (V_{CM}) = -IN, +IN = 12 V, and $V_{REF}1 = V_{REF}2$ = 2.5 V, unless otherwise noted.

Table 1. Electrical Specifications

Parameter	Test Conditions/Comments	Min	Typ	Max	Unit
GAIN					
Initial			50		V/V
Error Over Temperature	Specified temperature range			0.15	%
Gain vs. Temperature				±6	ppm/°C
VOLTAGE-OFFSET	Referred to input (RTI)				
Over Temperature	Specified temperature range[1]			±200	µV
Offset Drift	Box method (see Figure 56)		±0.26	±0.75	µV/°C
	Bowtie method (−40°C to 25°C) (see Figure 57)			±2.03	µV/°C
	Bowtie method (25°C to 125°C) (see Figure 57)			±1.65	µV/°C
INPUT					
Total Input-Bias Current[2]	+IN = −IN = 0 V, $V_S = V_{REF}1$ = 5 V, $V_{REF}2$ = 0 V	−11.0			µA
	+IN = −IN = 12 V, $V_S = V_{REF}1 = V_{REF}2$ = 0 V, T_A = 25°C		44		µA
	+IN = −IN = 12 V, $V_S = V_{REF}1$ = 5 V, $V_{REF}2$ = 0 V			175	µA
	+IN = −IN = 48 V, $V_S = V_{REF}1 = V_{REF}2$ = 0 V, T_A = 25°C		178		µA
	+IN = −IN = 48 V, $V_S = V_{REF}1$ = 5 V, $V_{REF}2$ = 0 V			484	µA
Input Offset Current	+IN = −IN = 0 V			1.0	µA
	+IN = −IN = 12 V			2.5	µA
	+IN = −IN = 48 V			2.7	µA
Input Voltage Range	Common mode, continuous	−2		+70	V
Common-Mode Rejection Ratio (CMRR)	Specified temperature range, DC, V_{CM} = −2 V to +70 V	123	142		dB
	T_A = 25°C, frequency = 10 kHz		110		dB
	T_A = 25°C, frequency = 50 kHz		96		dB

Change to 350uA