



Product/Process Change Notice - PCN 23_0281 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title:	AD8417-2 WLCSP Data Sheet Revision
Publication Date:	10-Jan-2024
Effectivity Date:	10-Jan-2024 <i>(the earliest date that a customer could expect to receive changed material)</i>
Revision Description:	Initial Release.

Description Of Change:

Data sheet revision:

- 1) Update pin configuration figure and table to align with actual product pin configuration.

See supporting documents section of this PCN for details.

Reason For Change:

The data sheet is being revised to reflect actual product performance.

Impact of the change (positive or negative) on fit, form, function & reliability:

No impact on fit, form, function, or reliability. Data sheet change only. No change to the product.

Summary of Supporting Information:

Changes will be reflected in Product Data Sheet Rev. B.

Supporting Documents

Attachment 1: Type: Revised Datasheet Specification

[ADI PCN 23_0281 Rev - AD8417-2 Data Sheet Revision.pdf...](#)

Note: If applicable, the device material declaration will be updated due to material change.

ADI Contact Information:

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:

PCN_Americas@analog.com

Europe:

PCN_Europe@analog.com

Japan:

PCN_Japan@analog.com

Rest of Asia:

PCN_ROA@analog.com

Appendix A - Affected ADI Models:

Added Parts On This Revision - Product Family / Model Number (1)

AD8417-2 / AD8417-2ACBZ-R7

Appendix B - Revision History:

Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	10-Jan-2024	10-Jan-2024	Initial Release.



AHEAD OF WHAT'S POSSIBLE™

AD8417-2 WLSCP

DATA SHEET REVISION

Revision Description

► FROM

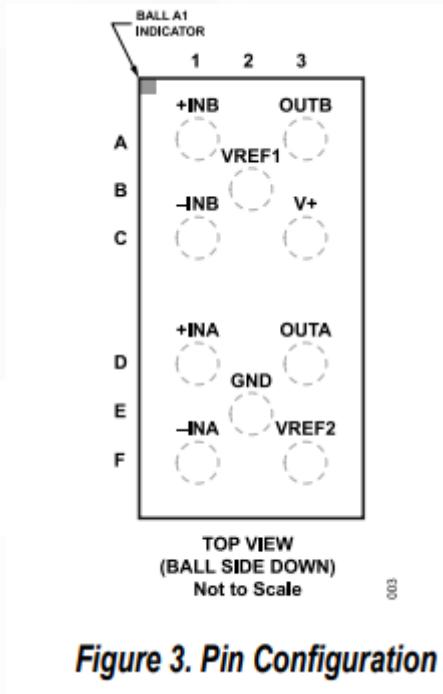


Figure 3. Pin Configuration

► TO

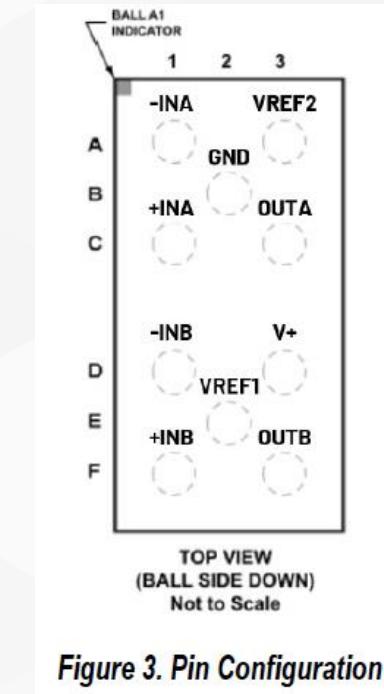


Figure 3. Pin Configuration

Revision Description

► FROM

Table 5. Pin Function Descriptions

Pin No.	Mnemonic	Description
A1	+INB	Positive Input, Channel B
A3	OUTB	Output, Channel B
B2	$V_{REF}1$	Reference Input 1
C1	-INB	Negative Input, Channel B
C3	V+	Supply
D1	+INA	Positive Input, Channel A
D3	OUTA	Output, Channel A
E2	GND	Ground
F1	-INA	Negative Input, Channel A
F3	$V_{REF}2$	Reference Input 2

► TO

Table 5. Pin Function Descriptions

Pin No.	Mnemonic	Description
A1	-INA	Negative Input, Channel A
A3	$V_{REF}2$	Reference Input 2
B2	GND	Ground
C1	+INA	Positive Input, Channel A
C3	OUTA	Output, Channel A
D1	-INB	Negative Input, Channel B
D3	V+	Supply
E2	$V_{REF}1$	Reference Input 1
F1	+INB	Positive Input, Channel B
F3	OUTB	Output, Channel B

Notes:

- ▶ No impact on fit, form, function or reliability.
- ▶ This is a data sheet change only, no changes to be done on the product.