



Product/Process Change Notice - PCN 20_0318 Rev. -

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title: LT3071 Data Sheet Limit Change

Publication Date: 14-Oct-2020

Effectivity Date: 16-Jan-2021 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Initial Release

Description Of Change:

Minor changes to the LT3071 product data sheet:

BIAS Pin Current In Nap Mode maximum specification (Page 3) is changed from 320 μ A to 420 μ A.

BIAS Pin Current In Nap Mode typical specification (Page 3) is changed from 200 μ A to 300 μ A.

Reason For Change:

The data sheet is being updated to accurately reflect device capabilities.

Impact of the change (positive or negative) on fit, form, function & reliability:

The change described above has no impact on fit, form, or reliability of the device.

Product Identification *(this section will describe how to identify the changed material)*

The product shipped after effectivity date will be tested to the new limit.

Summary of Supporting Information:

Changes will be reflected on the new product data sheet revision D. See changes on Electrical Characteristics table on page 3.

Supporting Documents

Attachment 1: Type: Datasheet Specification Comparison

ADI_PCN_20_0318_Rev_-_ADI PCN 20_0318 - Rev - Data Sheet - 3071 - 2020-10-13 - Update Nap Current.pdf

Attachment 2: Type: Datasheet Specification Comparison

ADI_PCN_20_0318_Rev_-_ADI PCN 20_0318 - Rev - Data Sheet - 3071 - 2020-10-13 - Spec Comparison.pdf

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:
PCN_Americas@analog.com

Europe:
PCN_Europe@analog.com

Japan:
PCN_Japan@analog.com

Rest of Asia:
PCN_ROA@analog.com

Appendix A - Affected ADI Models				
Added Parts On This Revision - Product Family / Model Number (8)				
LT3071 / LT3071EUFDPBF	LT3071 / LT3071EUFDPBTRMPBF	LT3071 / LT3071EUFDPBTRPBF	LT3071 / LT3071IUFDPBF	LT3071 / LT3071IUFDPBTRMPBF
LT3071 / LT3071IUFDPBTRPBF	LT3071 / LT3071MPUFD#PBF	LT3071 / LT3071MPUFD#TRPBF		

Appendix B - Revision History			
Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	14-Oct-2020	16-Jan-2021	Initial Release

Analog Devices, Inc.

DocId:8326 Parent DocId:None Layout Rev:7

ELECTRICAL CHARACTERISTICS The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. C_{OUT} = 15μF (Note 9), V_{IN} = V_{OUT} + 0.3V (Note 5), V_{BIAS} = 2.5V unless otherwise noted.

Previous Limit from Rev C, Page 3

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
BIAS Pin Current in Nap Mode	EN = Low	●	120	200	320	μA

New Limit on Rev D, Page 3

BIAS Pin Current in Nap Mode	EN = Low	●	120	300	420	μA
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