GJM Series Specifications and Test Methods(1)

			Specifications	Test Method			
No.	Item		Temperature Compensating Type				
1	1 Operating Temperature Range		−55 to +125°C	Reference Temperature: 25℃ (2C, 3C, 4C: 20℃)			
2	2 Rated Voltage		See the previous pages.	The rated voltage is defined as the maximum voltage who may be applied continuously to the capacitor. When AC voltage is superimposed on DC voltage, V ^{p,p} or whichever is larger, should be maintained within the rated voltage range.		ge, V ^{p.p} or V ^{o.p} ,	
3	Appearar	nce	No defects or abnormalities	Visual inspection			
4	Dimensio	ons	Within the specified dimensions	Using calipers			
5	5 Dielectric Strength		No defects or abnormalities	No failure should be observed when 300% of the rated voltage is applied between the terminations for 1 to 5 seconds, provided the charge/discharge current is less than 50mA.			econds,
6	Insulation Resistance (I.R.)		10,000M Ω min. or 500 Ω · F min. (Whichever is smaller)	The insulation resistance should be measured with a DC voltage not exceeding the rated voltage at 25℃ and 75%R max. and within 2 minutes of charging.			
7	7 Capacitance		Within the specified tolerance	The capacitance/Q should be measured at 25°C		at the	
			30pF and over: Q≧1000	frequency and voltage	e snown in th		
8	Q		30pF and below: Q≧400+20C	Frequency		1±0.1MHz	·
			C: Nominal Capacitance (pF)	Voltage		0.5 to 5Vrm	<u> </u>
		Temperature Coefficient	Within the specified tolerance (Table A)	The capacitance change should be measured after the each specified temperature stage. Temperature Compensating Type			fter 5 min. at
9	Capacitance Temperature Characteristics	Capacitance Drift	Within ±0.2% or ±0.05pF (Whichever is larger.)	The temperature coefficient is determined using the capacitance measured in step 3 as a reference. When cycling the temperature sequentially from step 1 through 5, (5C: +25 to 125°C: other temp. coeffs.: +20 to 125°C) the capacitance should be within the specified tolerance for the temperature coefficient and capacitance change as Table A. The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in steps 1, 3 and 5 by the capacitance value in step 3.			
				Step Temperature (°C)			C)
				1	Ref	erence Temp.	±2
				3	-55±3 Reference Temp. ±2		
				4	Kei	125±3	±2
				5	Ref	erence Temp.	±2
10	Adhesive Strength of Termination		No removal of the terminations or other defect should occur.	Solder the capacitor to the test jig (glass epoxy) Fig. 1 using a eutectic solder. Then apply a 5N* with the test jig for 10±1 sec. The soldering sho with an iron or using the reflow method and sho with care so that the soldering is uniform and fre as heat shock. Type a b GJM03 0.3 0.9 GJM15 0.4 1.5		orce in parallel Id be done either Id be conducted	
				Fig. 1			

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		Total grade.					
No. Item		Specifications	Test Method				
		Temperature Compensating Type					
	Appearance	No defects or abnormalities	Solder the capacitor to the test jig (glass epoxy board) in the				
	Capacitance	Within the specified tolerance	same manner and under the same conditions as (10). The capacitor should be subjected to a simple harmonic motio				
Vibration Resistance	e Q	30pF and over: Q≧1000 30pF and below: Q≧400+20C C: Nominal Capacitance (pF)	having a total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55Hz. The frequency range, from 10 to 55Hz and return to 10Hz, should be traversed in approximately 1 minute. This motion should be applied for a period of 2 hours in each of 3 mutually perpendicular directions (total of 6 hours).				
		No cracking or marking defects should occur.	Solder the capacitor to the test jig (glass epoxy boards) shown in Fig. 2 using a eutectic solder. Then apply a force in the direction shown in Fig. 3. The soldering should be done by the reflow method and should be soldered to the soldering should be soldered to the				
		\$4.5	be conducted with care so that the soldering is uniform and of defects such as heat shock. 20 150 Pressurizing speed: 1.0mm/sec.				
2 Deflect	on	100 t: 0.8mm	Pressurize				
		Type a b c GJM03 0.3 0.9 0.3	Flexure : ≦1				
		GJM15 0.4 1.5 0.5	Capacitance meter				
		(in mm) Fig. 2	45 45 (in mm)				
		9	Fig. 3				
Soldera Termina	bility of ation	75% of the terminations are to be soldered evenly and continuously.	Immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Preheat at 80 to 120°C for 10 to 30 seconds. After preheating, immerse in eutectic solder solution for 2±0.5 seconds at 230±5°C or Sn-3.0Ag-0.5Cu solder solution for 2±0.5 seconds at 245±5°C.				
		The measured and observed characteristics should satisfy the specifications in the following table.					
	Appearance	No marking defects					
Resistance		Within ±2.5% or ±0.25pF (Whichever is larger)	Preheat the capacitor at 120 to 150°C for 1 minute. Immerse the capacitor in a eutectic solder or Sn-3.0Ag-0.5Cu				
to Solderin Heat	Q	30pF and over: Q≥1000 30pF and below: Q≥400+20C C: Nominal Capacitance (pF)	solder solution at 270±5°C for 10±0.5 seconds. Let sit at room temperature for 24±2 hours.				
	I.R.	More than 10,000M Ω or 500 Ω · F (Whichever is smaller)					
	Dielectric Strength	No failure					
		The measured and observed characteristics should satisfy the specifications in the following table.	Fix the capacitor to the supporting jig in the same manner and under the same conditions as (10). Perform the five cycles				
	Appearance	No marking defects					
_ Temperature	Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	according to the four heat treatments listed in the following table Let sit for 24±2 hours at room temperature, then measure.				
5 Cycle		30pF and over: Q≥1000	Step 1 2 3 4				
	Q	30pF and below: Q≥400+20C C: Nominal Capacitance (pF)	Temp. (°C) Min. Operating Room Temp. ±3 Temp. Temp.				
	I.R.	More than 10,000M Ω or 500 Ω · F (Whichever is smaller)	Time (min.) 30±3 2 to 3 30±3 2 to 3				
	Dielectric Strength	No failure					
		The measured and observed characteristics should satisfy the specifications in the following table.					
	Appearance	No marking defects					
Humidity 6 Steady	Capacitance Change	Within ±5% or ±0.5pF (Whichever is larger)	Let the capacitor sit at 40±2°C and 90 to 95% humidity for 500±12 hours.				
State	Q	30pF and below: Q≧350 10pF and over, 30pF and below: Q≧275+ ½ C 10pF and below: Q≥200+10C C: Nominal Capacitance (pF)	Remove and let sit for 24±2 hours (temperature compensating type) at room temperature, then measure.				
	I.R.	More than $10,000M\Omega$ or $500\Omega \cdot F$ (Whichever is smaller)	†				
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NI -	Item		Specifications	- Test Method		
No.			Temperature Compensating Type			
			The measured and observed characteristics should satisfy the specifications in the following table.			
	Humidity Load	Appearance	No marking defects			
17		Capacitance Change	Within ±7.5% or ±0.75pF (Whichever is larger)	Apply the rated voltage at 40±2°C and 90 to 95% humidity for 500±12 hours. Remove and let sit for 24±2 hours at room temperature, then		
	Loud	Q	30pF and over: Q≥200 30pF and below: Q≥100+ ½° C C: Nominal Capacitance (pF)	measure. The charge/discharge current is less than 50mA.		
		I.R.	More than $500 \text{M}\Omega$ or $25 \Omega \cdot \text{F}$ (Whichever is smaller)			
			The measured and observed characteristics should satisfy the specifications in the following table.			
	High Temperature Load	Appearance	No marking defects			
18		Capacitance Change	Within ±3% or ±0.3pF (Whichever is larger)	Apply 200% of the rated voltage for 1000±12 hours at the maximum operating temperature ±3°C. Let sit for 24±2 hours (temperature compensating type) at room temperature, then		
10		Q 30pF and over: Q≥350 10pF and over, 30pF and below: Q≥275+ ½ C 10pF and below: Q≥200+10C C: Nominal Capacitance (pF)		measure. The charge/discharge current is less than 50mA.		
		I.R.	More than 1,000M Ω or 50 Ω · F (Whichever is smaller)			
19	ESR		0.1pF≦C≦1pF: $350m\Omega \cdot pF$ below 1pF <c≦5pf: <math="">300m\Omega below 5pF<c≦10pf: <math="">250m\Omega below</c≦10pf:></c≦5pf:>	The ESR should be measured at room temperature, and frequency 1±0.2GHz with the equivalent of BOONTON Model 34A.		
		10pF <c≦33pf: 400mω="" below<="" td=""><td colspan="3">The ESR should be measured at room temperature, and frequency 500±50MHz with the equivalent of HP8753B.</td></c≦33pf:>		The ESR should be measured at room temperature, and frequency 500±50MHz with the equivalent of HP8753B.		

Table A

	Temp. Coeff. (ppm/°C) *1	Capacitance Change from 25°C Value (%)					
Char. Code		− 55℃		-30℃		−10 ℃	
		Max.	Min.	Max.	Min.	Max.	Min.
5C	0±30	0.58	-0.24	0.40	-0.17	0.25	-0.11
6C	0±60	0.87	-0.48	0.60	-0.33	0.38	-0.21

^{*1:} Nominal values denote the temperature coefficient within a range of 25 to 125°C.

(2)

	(-)								
		Nominal Values (ppm/°C) *2	Capacitance Change from 20℃ Value (%)						
	Char.		–55°C		−25 ℃		−10 °C		
			Max.	Min.	Max.	Min.	Max.	Min.	
	2C	0±60	0.82	-0.45	0.49	-0.27	0.33	-0.18	
	3C	0±120	1.37	-0.90	0.82	-0.54	0.55	-0.36	
	10	0+250	2 56	-1 88	1 54	-1 13	1.02	-0.75	

^{*2:} Nominal values denote the temperature coefficient within a range of 20 to 125°C.