SPECIFICATION

May-03-2012

OF

LIQUID CRYSTAL DISPLAY MODULE

CUSTOMER: STD									
Model No. : UMSH-8596MD-20T									
Model version:									
Document Revision : 2									
Preliminary									
CUSTOMER APPROVED SIGNATURE									

This specification need to be signed by purchaser or customer as a specification of products production and delivery from URT. Without signature of this specification, any purchase order for this model no. will be treated and considered that this specification is automatically acknowledged and accepted by purchaser or customer.



Allen WangGeorge TsengAngus ChiuSharon TsaiMay-03-2012APPROVEDCHECKEDCHECKEDPREPAREDDate

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To Whom It May Concern:

In continuing to develop and promote the strategic partnership between United Radiant Technology (URT) and Microtips USA (MTUSA), URT is please to announce that we have entered into an agreement with MTUSA to support some key projects only through MTUSA and as such the attached spec with URT Part no. will be manufactured by URT but support and logistic of the sales will be handled by MTUSA.

URT is confident that this arrangement between our two companies will ultimately benefit the end customer.

Thank You.

Raymond Chen

Sales Manager: URT

Revision record								
Document	Model No.	Model No.						
Revision	Version No.	Description	by					
	UMSH-8596MD-T		Y.D. Shie					
0	(UFSH-K106EY-FT)	7.0" TFT.(backlight luminance 300cd/m²)	Zi Xin Ou					
	Version No. 0		25-May-2011					
1	UMSH-8596MD-1T	Modify the backlight luminance from 300cd/m² to	Y.D. Shie					
1	(UFSH-K106EY-FT)	500cd/m².	Zi Xin Ou					
	Version No. 0	Add the capacitive touch panel.	10-Jun-2011					
2	UMSH-8596MD-20T	(Two Finger multi Touch , two Fingers detection)	Y.D. Shie Zi Xin Ou					
	(UFSH-K106EY-FT) Version No. 0	2. Modify the module number from UMSH-8596MD-1T to UMSH-8596MD-20T.	03-May-2012					
//////////////////////////////////////	rrotips hnology Revision 2 ;	UMSH-8596MD-20T Ver. 0; May-03-2012	Page: 2					

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1. BASIC SPECIFICATION

1.1 Mechanical specifications

Items	Nominal Dimension	Unit
Active screen size	7.0" Diagonal	-
Dot Matrix	800 x RGB x 480	dots
Module Size (W x H x T)	165.0 x 106.4 x 8.2	mm.
Active Area (W x H)	152.4 x 91.44	mm.
Pixel Size (W×H)	0.1905 x 0.1905	mm.
Color depth	262K	color
Interface	Parallel 18-bit RGB	-
Driving IC Package	COG	-
Module weight	193	g

1.2 Display specification

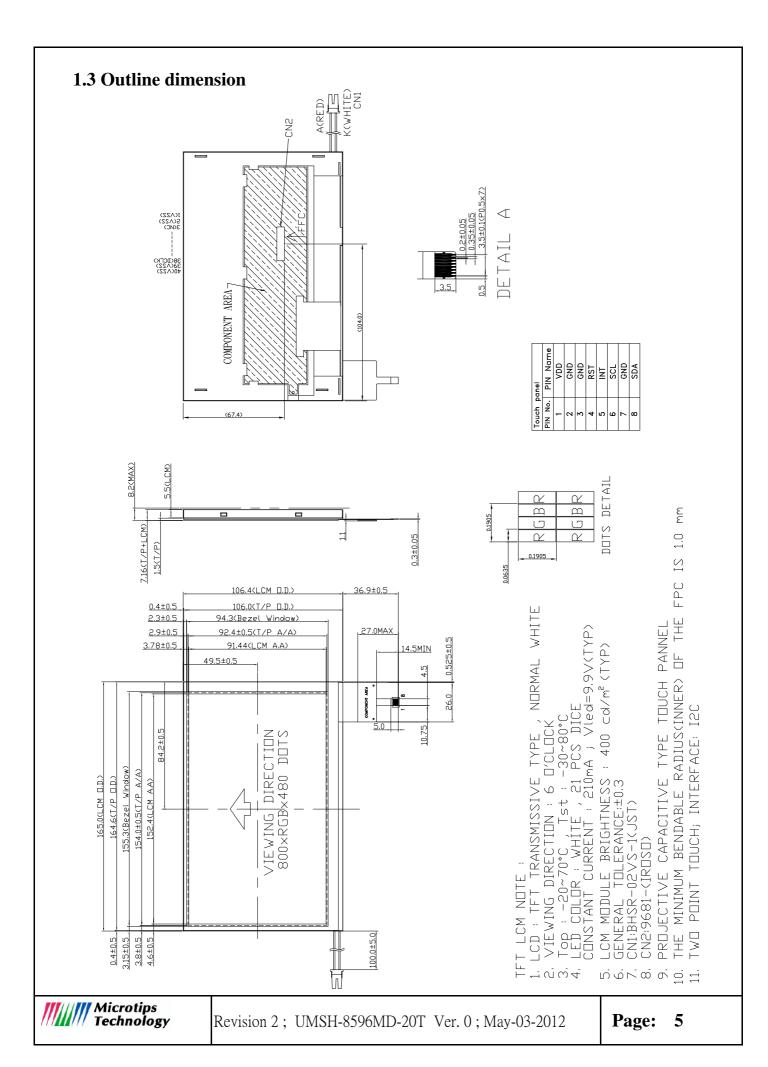
Display	Descriptions	Note
LCD Type	a-Si TFT	
LCD Mode	TN / Normal white	
Polarizer Mode	Transmissive	
Polarizer Surface	Normal	
Pixel arrangement	RGB-stripe	
Backlight Type	LED	
Viewing Direction(Gray inversion)	6 O'clock Direction	1

^{*}Color tone is slightly changed by temperature and driving voltage.

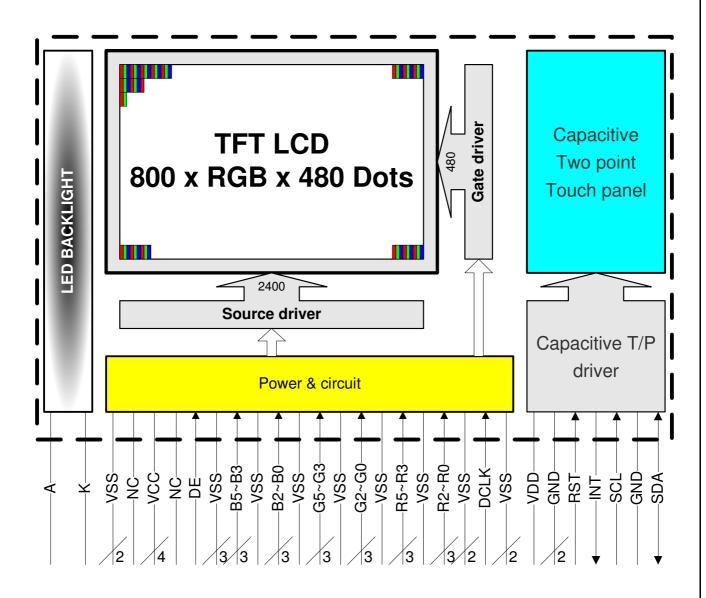
Note 1 : The viewing direction defined in this specification follows the rubbing direction of its mother TFT surface treatment.

The grayscale inversion is at this direction as well. The optimized viewing direction applied into end-device is decided by customers.

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1.4 Block diagram:



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1.5 Interface pin

Pin No.	Pin Symbol	I/O	Description
1~2	VSS	Р	GND
3	ИC	-	No connection
4~7	VCC	Р	Power supply for Module (+3.3V)
8	ИC	-	No connection
9	DE	I	Data enable
10~12	VSS	P	GND
13~15	B5~B3	I	Blue data input
16	VSS	P	GND
17~19	B2~B0	I	Blue data input
20	VSS	P	GND
21~23	G5~G3	I	Green data input
24	VSS	P	GND
25~27	G2~G0	I	Green data input
28	VSS	P	GND
29~31	R5~R3	I	Red data input
32	VSS	P	GND
33~35	R2~R0	I	Red data input
36~37	VSS	Р	GND
38	DCLK	I	Dot clock
39~40	VSS	P	GND

B/L interface pin:

Pin No.	Pin Symbol	I/O	Description
1	A	P	Power supply for LED+
2	K	P	Power supply for LED-

|Capacitive touch panel (I2C) Interface:

_		•	
Pin No.	Pin Symbol	I/O	Description
1	VDD	P	Power supply. (+3.3V)
2~3	GND	P	Ground.
4	RST	I	System reset signal input, active low. Note (1)
5	ТИІ	0	Active low when data output from touch panel.
6	SCL	I	Serial Clock.
7	GND	Р	Ground.
8	SDA	I/O	Serial data access.

Note (1) Reset pin is low active and needs hold low for 1ms to take effect.



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2. ELECTRICAL CHARACTERISTICS

2.1 Absolute Maximum Ratings

Items	Symbol	Min.	Max.	Unit
Power supply voltage	VCC	-0.3	7.0	V
rower supply voltage	VDD	2.4	3.6	V
Input voltage	Vin	-0.3	VCC+0.3	V
Operate temperature range	Тор	-20	70	°C
Storage temperature range	Тѕт	-30	80	°C

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2.2 DC Characteristics

 $T_a=25$ °C

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Items	Symbol	Min.	Тур.	Max.	Unit	Condition
	VCC	-	3.3	-	V	-
Supply voltage	VDD	-	3.3	-	V	-
	V _{IL}	0	-	0.3VCC	V	L level
Input Voltage	V_{IH}	0.7VCC	-	VCC	V	H level
	$I_{ t VCC}$	-	160	250	mA	Note 1
Current consumption	$\mathbf{I}_{ ext{VDD}}$	-	6	12	mA	-

*Note1:

Measuring Condition:

Standard Value MAX.

 $Ta = 25^{\circ}C$

VCC - GND = 3.3V

 $Display\ Pattern = Check\ pattern$



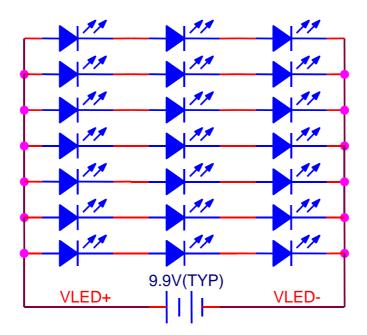
0 gray black pattern

2.3 Back-light Characteristics

PARAMETER	SYMBOL	MIN	TYP	MAX	Unit	Test Condition	NOTE
Supply Current	If	-	210	-	mA	Ta=25℃	-
Supply Voltage	Vf	-	9.9	-	V	Ta=25℃	-
Half-Life Time	Lf	-	50000	-	hrs	Ta=25℃	1

Note 1 : The "Half-Life Time" is defined as the LED chip brightness decreases to 50% than original brightness, Based on Ta $25\pm2\%$,60 $\pm10\%$ RH condition .

Note 2: LED backlight is 21 LEDs.



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2.4 AC Characteristics

Switching characteristics

PARAMETER	Symbol		Unit		
TANAMETER	Symbol	Min.	Тур.	Max.	Offic
Data setup time	T_{dsu}	6	-	lo llo ll	ns
Data hold time	T_{dhd}	6	J	ř	ns
DE setup time	T_{esu}	6	-		ns
Source output settling time	T_{ST}	-	=	15	μs
Source output loading R	$R_{\scriptscriptstyle{SL}}$	-	2		K ohm
Source output loading C	C_{SL}	-	60		рF

Parallel RGB Input Timing Requirement

DE mode

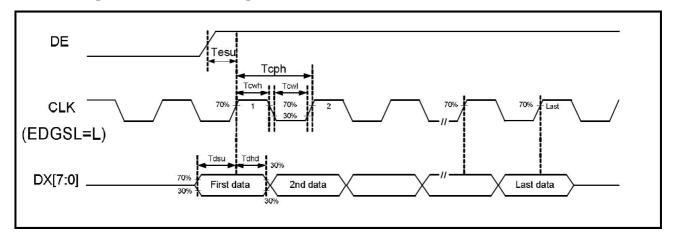
PARAMETER	Cymbal		Spec.		1154
PARAMETER	Symbol	Min.	Тур.	Max.	Unit
CLK frequency	F _{CPH}	-	33.26	Ħ	MHz
CLK period	T _{CPH}	=	30.06	-	ns
CLK pulse duty	T _{CWH}	40	50	60	%
DE period	T _{DEH} +T _{DEL}	1000	1056	1200	T _{CPH}
DE pulse width	T _{DEH}	=	800	=	T _{CPH}
DE frame blanking	T_DEB	10	45	110	T _{DEH} +T _{DEL}
DE frame width	T_DE	-	480	-	T _{DEH} +T _{DEL}

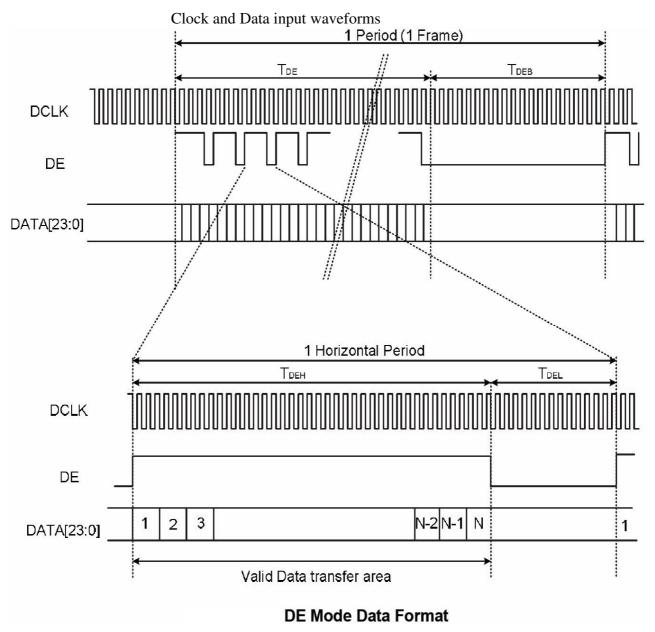
PARAMETER	Symbol		Spec.		Unit
TANAMETER	Syllibol	Min.	Тур.	Max.	Offic
OEV pulse width	T _{OEV}	.=	150	-	T _{CPH}
CKV pulse width	T _{CKV}		133	-	T _{CPH}
DE(internal)-STV time	T_1	:=	4	-	T_{CPH}
DE(internal)-CKV time	T ₂	H	40	=	T_{CPH}
DE(internal)-OEV time	T ₃	æ	23	-	T_{CPH}
DE(internal)-POL time	T_4	_	157	=	T_{CPH}
STV pulse width	=	=	1	=	T _H

⁽i). T_{HS}+T_{HA}<T_H

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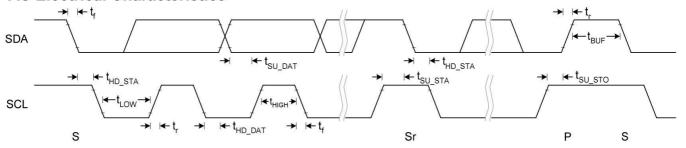
Timing Controller Timing Chart





2.5 Capacitive touch panel controller AC Characteristics

AC Electrical Characteristics



I2C Fast Mode Timing

I2C Fast Mode Timing Characteristic

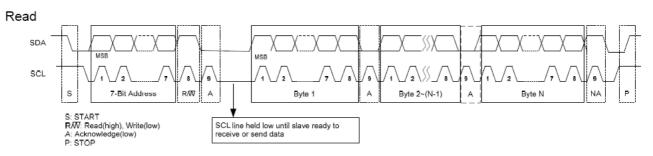
Conditions: VDD = IOVDD = 3.3V, GND = 0V, $T_A = 25$ °C

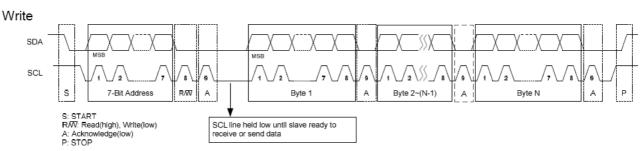
Symbol	Parameter		Rating		Unit
Cymbol	r arameter	Min.	Тур.	Max.	Oilit
f_{SCL}	SCL clock frequency	0	-	400	kHz
t_{LOW}	Low period of the SCL clock	1.3			us
t _{HIGH}	High period of the SCL clock	0.6	(-	-	us
t_f	Signal falling time		-	300	ns
t _r	Signal rising time	-	i.— i	300	ns
t _{SU_STA}	Set up time for a repeated START condition	0.6	-	-	us
t _{HD_STA}	Hold time (repeated) START condition. After this period, the first clock pulse is generated	0.6	-1	-	us
t _{SU_DAT}	Data set up time	100	-	-	ns
t _{HD DAT}	Data hold time	0	o = 0	0.9	us
t _{su_sto}	Set up time for STOP condition	0.6	-	-	us
t _{BUF}	Bus free time between a STOP and START condition	1.3	-	-	us
C _b	Capacitive load for each bus line	-	1-	400	pF

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2.6 I2C Host Interface Protocol

I2C Slave Interface





2.6.1 Register Read

For reading register value from I2C device, host has to tell I2C device the *Start Register Address* before reading corresponding register value.



Figure 1. Register Read Format.

ST1232/ST1332 I2C host interface protocol supports *Repeated Register Read*. That is, once the *Start Register Address* has been set by host, consequent I2C Read(R) transactions will directly read register values starting from the *Start Register Address* without setting address first, as shown in Figure 2.

I2C Start	I2C Value Header of (R) Reg(a)	Value of Reg(a+1)		Value of Reg(a+n)	I2C Stop	I2C Start	I2C Header (R)	Value of Reg(a)	Value of Reg(a+1)		Value of Reg(a+n)	I2C Stop	
--------------	--------------------------------	----------------------	--	----------------------	-------------	--------------	----------------------	-----------------------	----------------------	--	----------------------	-------------	--

Figure 2. Repeated Register Read.

Header Value : 0xab

2.6.2 Register Write

For writing register to I2C device, host has to tell I2C device the Start Register Address in each I2C Register Write transaction. Register values to the I2C device will be written to the address starting from the Start Register Address described in Register Write I2C transaction as shown in Figure 3.

b.		I2C	Start					
	I2C	Header	Reg.	Value to	Value to	Value to	I2C	
	Start	(W)	Addr.	Reg(a)	Reg(a+1)	 Reg(a+n)	Stop	
		(00)	(a)					

Figure 3. Register Write Format.

Header Value: 0xaa



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2.6.3 Registers

ST1332 provides a register set for host to configure device attributes and retrieve information about fingers,

			Host Inte	rface Registe	ers (Report F	Page)			
Reg Addr.	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
0x00					Doce	en/od			
0x01			Reserved						
0x02	Device Control Reg		Rese	erved		Reserved	Reserved	Power Down (R/W)	Reset (R/W)
0x03	Timeout to Idle Register		Timeout to Idle (Sec) (R/W)						
0x04 ~ 0xQE			Reserved						
0x10	Fingers		Reserved Fingers(RO)						
0x11					Rese	erved			
0x12	XY0 Coord (High Byte)	Valid 0 (RO)		X0_H(RO)		Reserved		Y0_H (RO)	
0x13	X0 Coord (Low Byte)				X0_L	_(RO)			
0x14	Y0 Coord (Low Byte)				Y0_L	_(RO)			
0x15	XY1 Coord (High Byte)	Valid 1 (RO)	Y1 H(PO) Peserved V1 H(PO)						
0x16	X1 Coord (Low Byte)	X1_L(RO)							
0x17	Y1 Coord (Low Byte)	-	Y1_L(RO)						

2.6.4 Device Control Register

Reg Addr.	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
0x02	Device Control Reg		Rese	rved		Reserved	Reserved	Power Down (R/W)	Reset (R/W)

Device Control Register provides device control bits for host to reset the device, power down the device.

2.6.5 Timeout to Idle Register

Reg Addr.	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	
0x03	Timeout to Idle Register		Timeout to Idle (Sec) (R/W)							

Timeout to Idle Register provides timeout control to entering Idle Mode for host.

The touch controller will enter Idle Mode after the number of seconds specified in Timeout to Idle Register if there is no touch detected in this period.

Set the field to 0xFF will disable Idle Mode. Set the field to 0 will entering Mode immediately.

The default value of Timeout to Idle Register is set to 0x08 for 8 seconds to Idle Mode.

2.6.6 Fingers Register

Reg Addr.	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
0x10	Fingers		Rese	erved			Finger	rs(RO)	

Fingers field represents number of fingers detected by touch controller.

The coordinates of each finger detected are represents in X Coordinate and Y Coordinate fields.

2.6.7 XY Coordinate Registers

Reg Addr.	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	
0x12	XYO Coord (High Byte)	Valid 0 (RO)		X0_H(RO) Reserved				Y0_H (RO)		
0x13	XO Coord (Low Byte)		XO_L(RO)							
0x14	YO Coord (Low Byte)		Y0_L(RO)							
0x15	XY1 Coord (High Byte)	Valid 1 (RO)	X1_H(RO) Reserved Y1_H (RO)							
0x16	X1 Coord (Low Byte)	X1_L(RO)								
0x17	Y1 Coord (Low Byte)	Y1_L(RO)								

XY Coordinate Registers represent the XY coordinates for each touch point ID.

Valid bit field tells that this point ID is valid and the XY information represents a real touch point on touch sensor.



3. OPTICAL CHARACTERISTICS

3.1 Characteristics

Electrical and Optical Characteristics

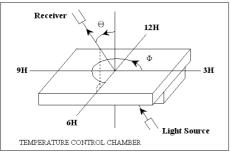
No.	Item		symb	ool / temp.	Min.	Тур.	Max.	Unit	Note
1	Response	Time	Tr	25	NA	5	10	ms	2
			Tf	25	NA	15	20	1115	
		Hor.	2+		60	85	-		
2	Viewing	1101.	2-	Center	60	85	-	degree	3
~	Angle	Ver.	1+	CR>=10	60	85	-	degree	3
		VCI.	1-		60	85	-		
3	Contrast R	Ratio	Cr	25	700	1000	-	-	4
	Red x-cod	e	Rx		0.54	0.58	0.64]	
	Red y-cod	e	Ry		0.30	0.35	0.40		
	Green x-co	ode	Gx		0.30	0.35	0.40		
	Green y-co	ode	Gy		0.52	0.57	0.62		5
4	Blue x-coo	de	Bx	25	0.10	0.15	0.20	_	
	Blue y-coo	de	By		0.08	0.13	0.18		
	White x-co	ode	Wx		0.26	0.31	0.36		
	White y-code		Wy		0.29	0.34	0.39		
	Brightness	3	Y		300	400	-	cd/m ²	
5	Brightness Uniformit			25	80	-	-	%	6

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3.2 Definition of optical characteristics

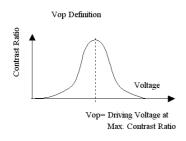
Measurement condition:

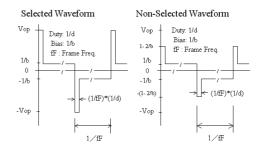
Transmissive and Transflective type



PHOTAL LCD-5000

[Note 1] Definition of LCD Driving Vop and Waveform :





[Note 2] Definition of Response Time

for Positive type:

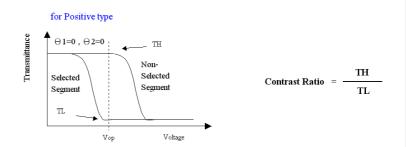
Selected State Non-Selected State Selected State

10%

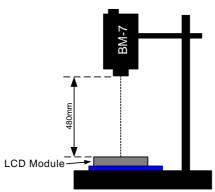
Tr Tr time

[Note 3] Definition of Viewing Angle:

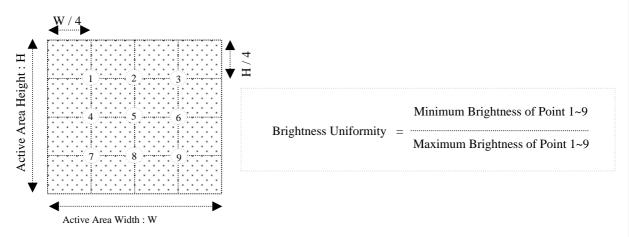
[Note 4] Definition of Contrast Ratio:



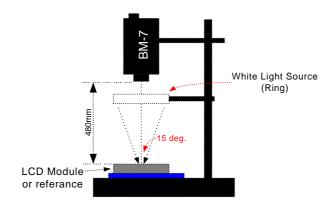
[Note 5] Definition of measurement of Color Chromaticity and Brightness



[Note 6] Definition of Brightness Uniformity



[Note 7] Definition of Measurement of Reflectance





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4. RELIABILITY:

Item No	Items	Condition					
1	High temperature operating	70 °C , 200 hours					
2	Low temperature operating	-20 °C , 200 hours					
3	High temperature storage	80 °C , 200 hours					
4	Low temperature storage	-30 °C , 200 hours					
5	High temperature & humidity storage	60°C, 90%RH, 100 hours					
6	Thermal Shock storage	-30°C, 30min.<=> 80°C, 30min. 10 Cycles					
7	Vibration test	10 => 55 => 10 => 55 => 10 Hz, within 1 minute Amplitude: 1.5mm. 15 minutes for each Direction (X,Y,Z)					
8	Drop test	Packed, 100CM free fall, 6 sides, 1 corner, 3edges					
9	Life time	50,000 hours 25°C, 60%RH, specification condition driving					

- * One single product test for only one item.
- * Judgment after test: keep in room temperature for more than 2 hours.
 - Current consumption < 2 times of initial value
 - Contrast > 1/2 initial value
 - Function : work normally

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5. PRODUCT HANDLING AND APPLICATION

PRECAUTION FOR HANDLING LCM

The LCD module contains a C-MOS LSI. People who operate the LCM should wear

ESD protection eguipement to prevent ESD hurt on products.

Do not input any signal before power is turned on.

Do not take LCM from its packaging bag until it is assembled.

Peel off the LCM protective film slowly since static electricity may be generated.

Pay attention to the humidity of the work shop, 50~60%RH is satisfactory.

Use a non-leak iron for soldering LCM.

Do not touch the display surface or connection terminals area with bare hands. Smudges on the display surface reduce the insulation between terminals.

Cautions for soldering to LCM:

Condition for soldering I/O terminals:

Temperature at iron tip: 350 ± 15 .

Soldering time: 3~4sec./ terminals.

Type of solder: Eutectic solder(rosin flux filled).

PRECAUTION IN USE OF LCM

Do not contact or scratch the front surface and the contact pads of a LCM with hard materials such as metal or glass or with one's nail.

To clean the surface, wipe it gently with soft cloth dampened by alcohol.

Do not attempt to wiped off the contact pads.

Keep LCM panels away from direct sunlight, also avoid them in high-temperature & high humidity environment for a long period.

Do not drive LCM by DC voltage.

Do not expose LCM to organic solvent.

Liquid in LCM is hazardous substance. In case a contact with liquid crystal material is occured, be sure to immediately wash such material away by soap and water.

The polarizer is easily damaged and should be handle with special care. Don't press or rub it with hard objects.

PRECAUTION FOR STORING AND USE OF LCM

To avoid degradation of the device , do not store the module under the conditions of direct sunlight , high temperature or high humidity . Keep the module in bags designed to prevent static electricity charging under low temperature / normal humidity conditions(avoid high temperature / high humidity and low temperature below 0)

Never use the LCD , LCM under 45 Hz , the liquid crystal will decomposition and cause permently damage on display !!

USING ON MEDICAL CARE, SAFETY OR HAZARDOUS APPLICATION OR SYSTEM

For the application in medical care, safety and hazardous products or systems, an authorization from URT is required. URT will not responsible for any damage or loss which caused by the products without any authorization given by URT.

This product is not allowed to be designed and used for military application and/or purpose.

The delivery of this product to the countries and/or regions where the embargoes are imposed by U.N. is prohibited.

The application and delivery of this product must comply with Startegic High-Tech Commodities (SHTC) export control and the sales to the embargoed and/or sanctioned countries or regions are strictly prohibited.



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6. DATE CODE OF PRODUCTS

Date code will be shown on each product :

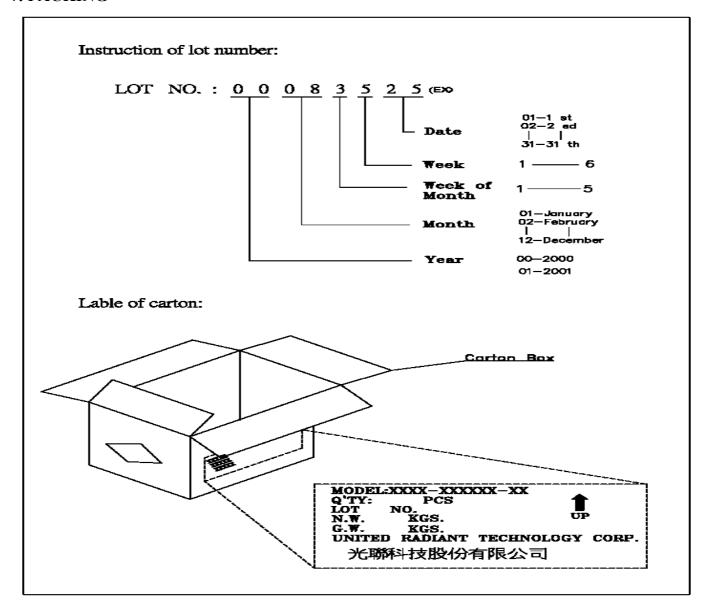
$$YY = MM = DD - XXXX$$

Year Month Day - Production lots

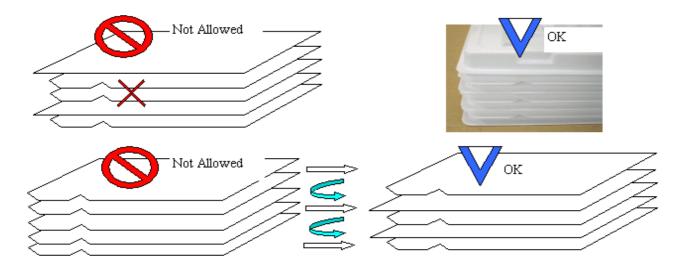
Example: 090508 - 0 0 0 3 ==>Year 2009, May.,08rd, Batch no.03

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7. PACKING

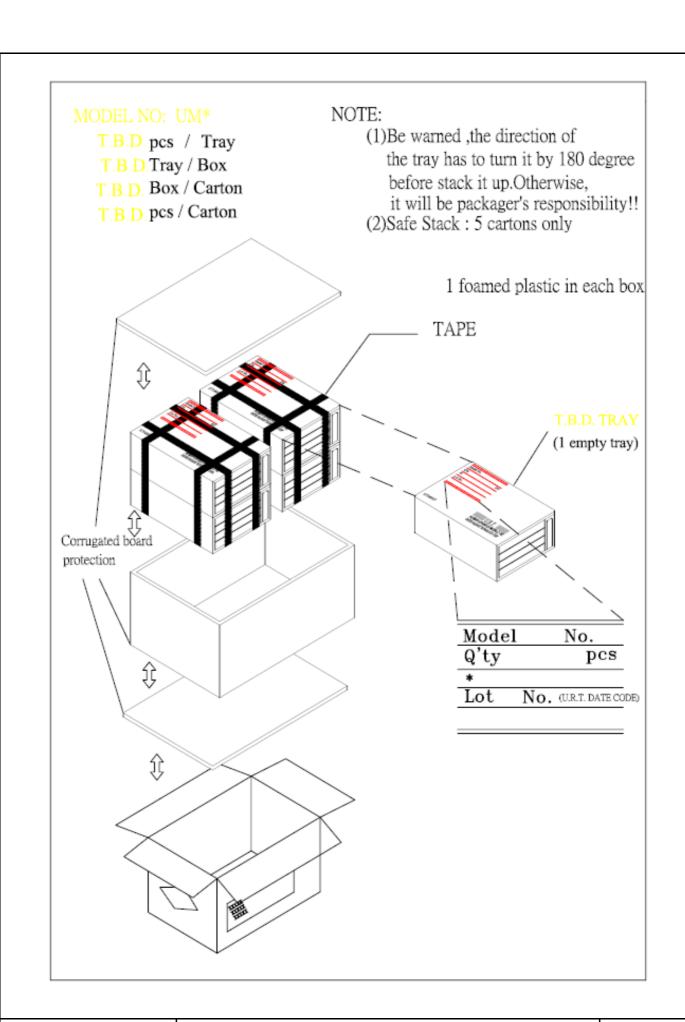


Packing tray must be stacked with alternated direction to each others. To tacks packing trays in same direction will cause product damaged.



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8. INSPECTION STANDARD

8.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

8.1.1. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM U.R.T. TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 TO 40 ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

8.1.2. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION, A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (or MIL-STD-105E), LEVEL SINGLE PLAN.

CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %
TOTAL	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION , A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

8.1.3. WARRANTY POLICY

U.R.T. WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. U.R.T. WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF U.R.T.

8.2. CHECKING CONDITION

- **8.2.1.** CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.
- **8.2.2.** CHECKER SHALL SEE OVER 30 cm. WITH BARE EYES FAR FROM SAMPLE AND USING 2 PCS. OF 20W FLUORESCENT LAMP.

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8.3. INSPECTION PLAN:

CLASS	ITEM	JUDGEMENT	CLASS
	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO." , "LOT NO." AND "QUANTITY"	Minor
PACKING &		SHOULD INDICATE ON THE PACKAGE.	
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED	Critical
		QUANTITY SHORT OR OVERREJECTED	
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON	Major
		THE PRODUCT	
	4. DIMENSION,	ACCORDING TO SPECIFICATION OR	
ASSEMBLY	LCD GLASS SCRATCH	DRAWING.	Major
	AND SCRIBE DEFECT.		
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE	Minor
		IS VISABLE IN THE VIEWING AREA	
		REJECTED	
	6. BLEMISH、BLACK SPOT、	ACCORDING TO STANDARD OF VISUAL	Minor
	WHITE SPOT IN THE LCD	INSPECTION (INSIDE VIEWING AREA)	
	AND LCD GLASS CRACKS	,	
	7. BLEMISH、BLACK SPOT	ACCORDING TO STANDARD OF VISUAL	Minor
APPEARANCE	WHITE SPOT AND SCRATCH	INSPECTION (INSIDE VIEWING AREA)	
THI TENNINCE	ON THE POLARIZER		
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL	Minor
		INSPECTION (INSIDE VIEWING AREA)	
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR (OR NEWTON	
	7, 2023 Tala. 20 II 0020N	RING) OF LCDREJECTED.	Minor
		OR ACCORDING TO LIMITED SAMPLE	TVIIIIOI
		(IF NEEDED, AND INSIDE VIEWING AREA)	
	10. ELECTRICAL AND OPTICAL	ACCORDING TO SPECIFICATION OR	Critical
	CHARACTERISTICS	DRAWING . (INSIDE VIEWING AREA)	Critical
	(CONTRAST, VOP,	DRAWING . (INSIDE VIEWING AREA)	
	CHROMATICITY ETC)		
ELECTRICAL	11.MISSING LINE	MISSING DOT, LINE, CHARACTER	Critical
ELLCTRICAL	TI.WIBBING ENVE	REJECTED	Cinical
	12 SHOPT CIRCUIT		Cuitical
	12.SHORT CIRCUIT,	NO DISPLAY, WRONG PATTERN	Critical
	WRONG PATTERN DISPLAY	DISPLAY, CURRENT CONSUMPTION	
	14 DOWNERSON (FOR COVER AND THE	OUT OF SPECIFICATION REJECTED	3.5
	13. DOT DEFECT (FOR COLOR AND TFT)	ACCORDING TO STANDARD OF VISUAL	Minor
		INSPECTION	

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8.4. STANDARD OF VISUAL INSPECTION CLASS JUDGEMENT (A) ROUND TYPE: unit: mm. DIAMETER (mm.) ACCEPTABLE Q'TY DISREGARD . BLEMISH、BLACK SPOT、 0.1 WHITE SPOT IN THE LCD. 0.2 0.1 < 2 0.2 < 0.25 1 0.25 < 0 . BLEMISH, BLACK SPOT, NOTE: =(LENGTH+WIDTH)/2 8.4.1 MINOR WHITE SPOT AND SCRATCH (B) LINER TYPE: unit: mm. ACCEPTABLE Q'TY ON THE POLARIZER LENGTH WIDTH W 0.03 DISREGARD 5.0 0.03 < W 0.05 3 5.0 0.05 <W 0.07 0.07 < W FOLLOW ROUND TYPE unit: mm. ACCEPTABLE Q'TY DIAMETER DISREGARD **BUBBLE IN POLARIZER** 0.15 8.4.2 MINOR 0.5 0.15 < 2 0.5 < Items ACC. Q'TY Dot Defect Bright dot N 4 Dark dot N Pixel Define G G R G В R В R В R G В R G В R G В 8.4.3 MINOR G R G R G В Not 1: The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot. Not 2: Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern. Not 3: Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green ,blue pattern. Microtips Technology Revision 0; UMNH-8120MD-7T Ver. 0; June-27-2011

NO.	CLASS	ITEM	JUDGEMENT	
8.4.4	MINOR	LCD GLASS CHIPPING	S	Y > S Reject
8.4.5	MINOR	LCD GLASS CHIPPING	SX	X or Y > S Reject
8.4.6	MAJOR	LCD GLASS GLASS CRACK	T	Y > (1/2) T Reject
8.4.7	MAJOR	LCD GLASS SCRIBE DEFECT	$\Lambda^{\frac{1}{7}}$	 a> L/3 , A>1.5mm. Reject B: ACCORDING TO DIMENSION
8.4.8	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL AREA)	T	= (x+y)/2 > 2.5 mm Reject
8.4.9	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL SURFACE)	TZX	Y > (1/3) T Reject
8.4.10	MINOR	LCD GLASS CHIPPING	T Z	Y > T Reject

