Application Guide
eSATA

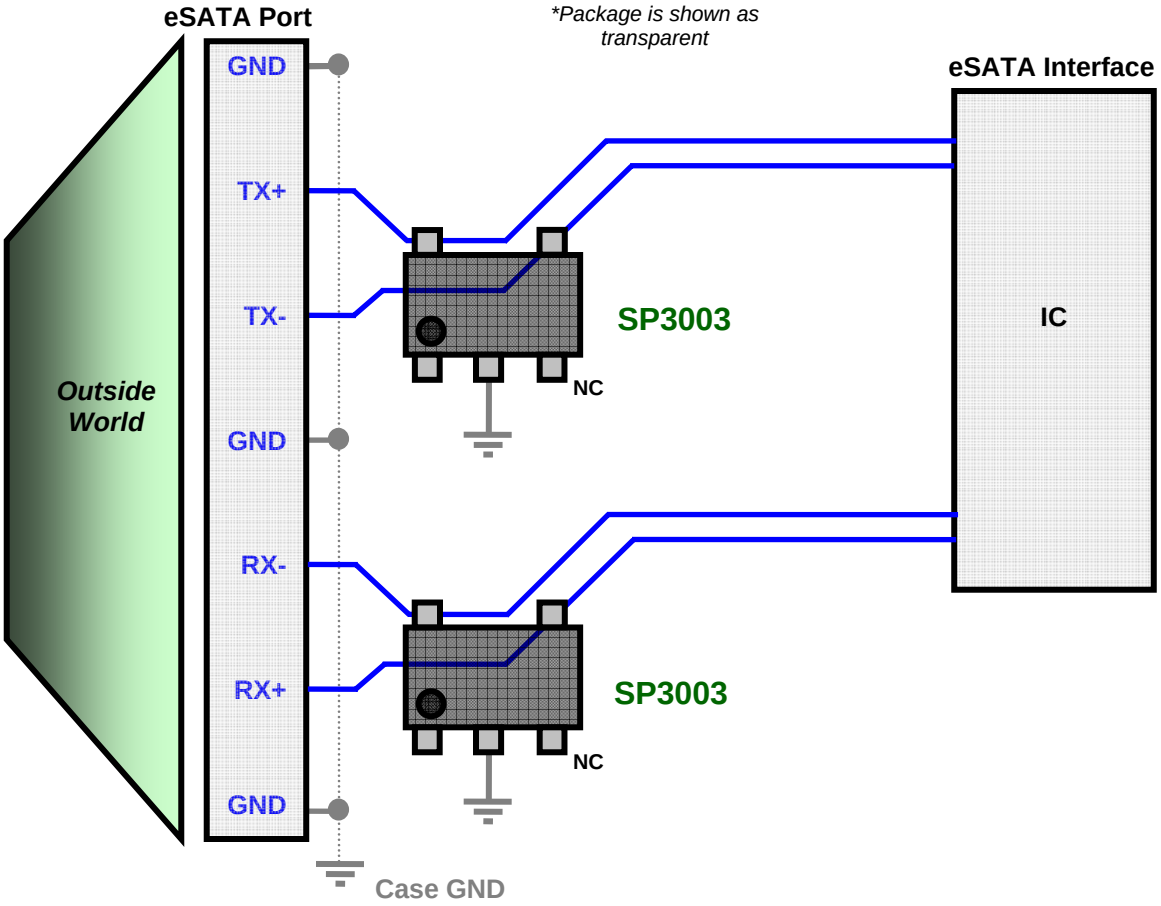
Considerations:

- eSATA is a subset of the SATA protocol that uses 2 differential pairs for communication
  - → Four lines need to be protected per port (i.e. TX+/TX- and RX+/RX-)
  - → Currently eSATA is capable of running raw data rates of 1.5Gbps (Gen 1) and 3.0Gbps (Gen 2).

- These high bus speeds require very low capacitance devices to prevent signal degradation

- To maintain the line impedance the designer should avoid using 90º angles and vias

Application Schematic:



Recommended SPA Devices:

Ordering Number	ESD Level (Contact)	I/O Capacitance	# of Channels	V_{RWM}	Packaging
SP3003-02XTG	±8kV	0.65pF	2	6V	SOT553
SP3010-04UTG	±8kV	0.45pF	4	6V	µDFN-10 (2.5x1.0mm)
SP3003-04JTG	±8kV	0.65pF	4	6V	SC70-6